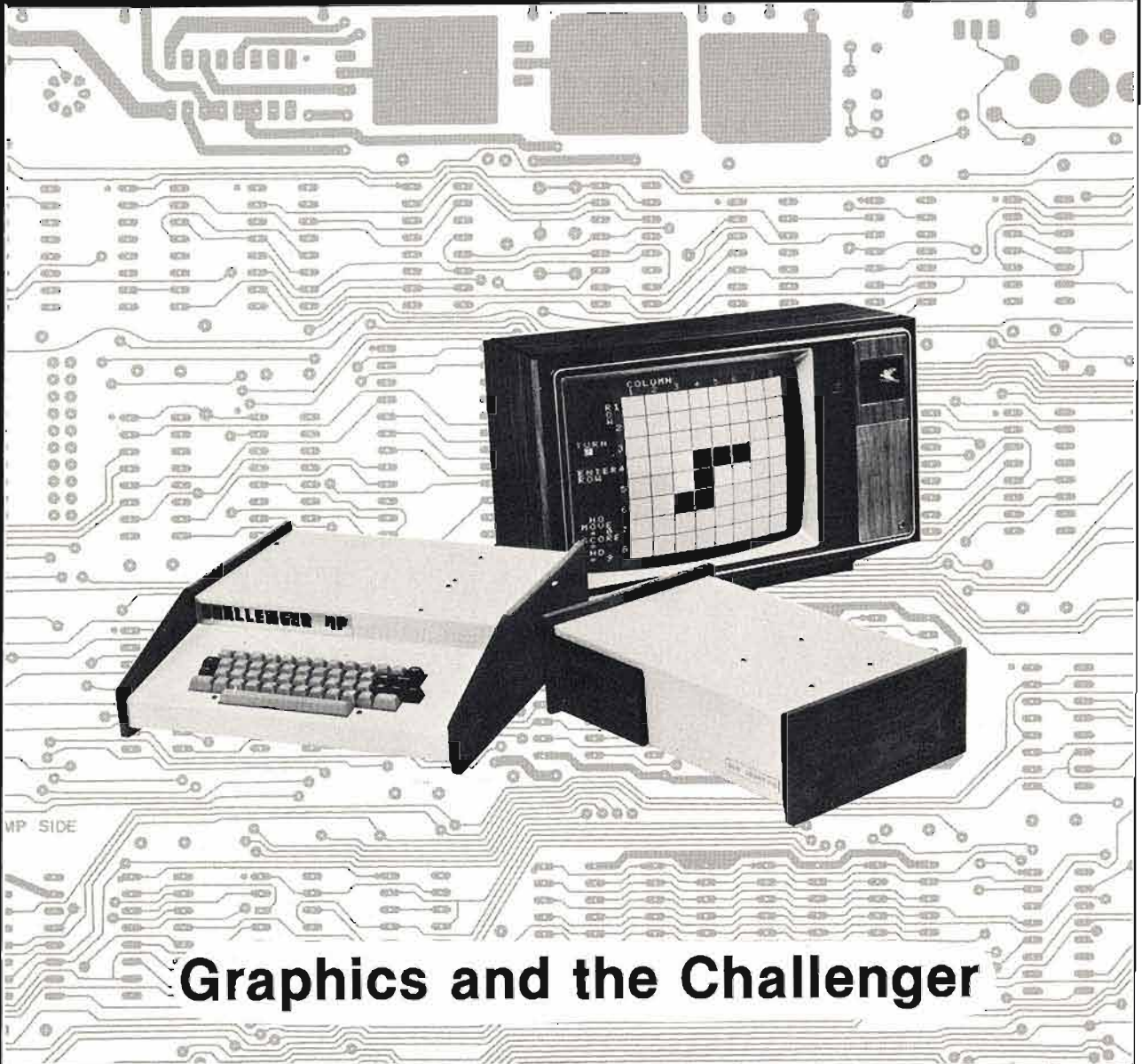


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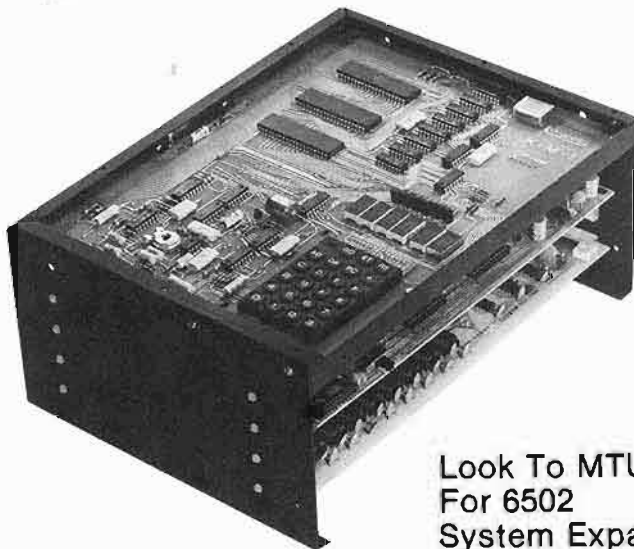
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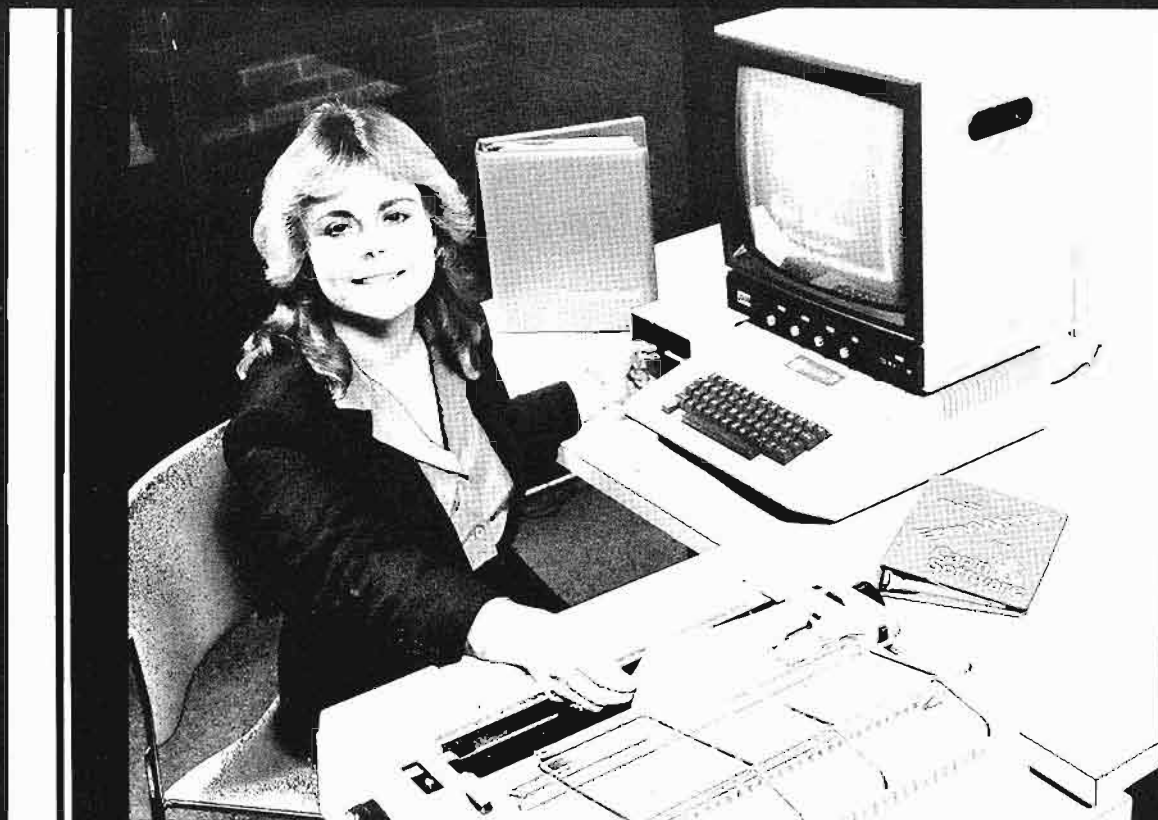
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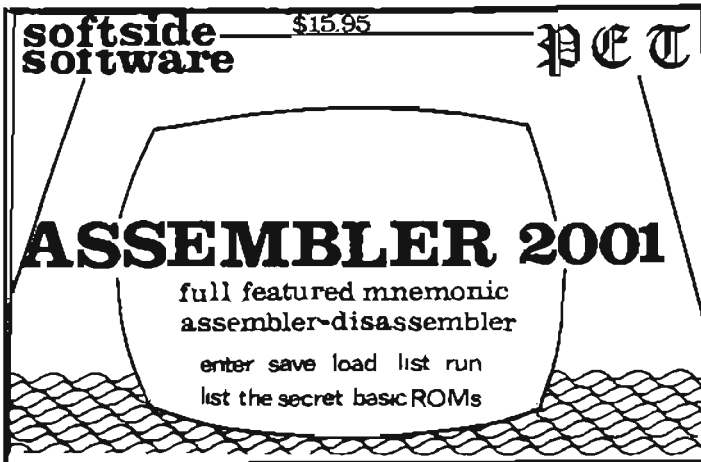


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February 1980
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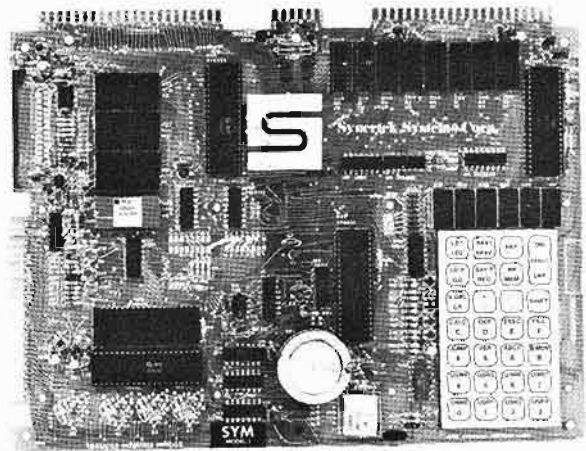
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Expanding the SYM-1... Adding an ASCII Keyboard

Adding an ASCII keyboard to a SYM is fairly simple, if you know what you are doing. There are a few tricks required and some understanding of the SYM Monitor is needed. And, it is all presented here.

Robert A. Peck
P.O. Box 2231
Sunnyvale, CA 94087

The Synertek monitor program has a feature which allows it to communicate directly with a teletype system. This is, when you are in the reset mode, the monitor will scan both the onboard keypad and the teletype input port to look for the first keystroke. After finding the first stroke, either the keypad or the teletype is used as the exclusive input to the monitor program.

Because of the teletype interface, it would, at first thought, be an excellent way to expand the basic SYM system. However, when one considers the bulk, cost and availability of a teletype, other alternatives for early stage expansion may come to mind.

Synertek also offers a keyboard/video display unit for the SYM-1, known as the KTM-2. It is a very versatile unit; but the present list price of \$349 could cause some of us to wait a bit to budget for its eventual purchase. What then to do in the meantime?

To at least begin a system expansion at a low cost, one might consider adding a full ASCII keyboard now and a full video display as a separate step at a later date. ASCII keyboards are available on the surplus scene for as little as \$35, so this seems like a good place to start.

An initial thought in adding the ASCII keyboard to the SYM would be to duplicate the functions of the teletype. This would pose a couple of unwelcome complications, specifically the choice of an appropriate baud rate and the addition of a parallel to serial conversion to the ASCII keyboard output.

However, if we attach the keyboard to the teletype input and log onto the keyboard, the SYM monitor will respond to us in bit serial mode as well. We would then, at least for a period of time, lose our display capabilities. We would have to restore the onboard display vector in order to see the results of our keystrokes.

Since a certain amount of software had to be written anyway to bypass the above problem, it seemed appropriate to solve some hardware problems with software instead. I added VIA No. 2 (6522) to the system to provide an extra set of input ports, one of which I dedicated to the parallel ASCII keyboard. Port B is used for the 6522 timer functions so to preserve these for future use.; Port A was chosen for the keyboard.

In the attempt to add the keyboard to the system, a number of items were kept in mind:

(A) All of the monitor functions had to be normally accessible (different key groups perhaps, but all functions still needed).

(B) The use of the keyboard in place of the keypad should not interfere with the execution of any programs I had already written or adapted for use with the SYM if at all possible.

(C) The interface routines should be written in a fully relocatable style so that they could be incorporated into a monitor PROM routine if desired.

In keeping with these principles, the program shown in Figure 1 was written to perform the monitor interfacing.

When one desires to use the external ASCII keyboard instead of the keypad, the routine labeled INIT would be executed. A direct jump to this routine is used. It modifies both the keyboard input vector and the keyboard status vector, providing for entry to the other routines. Then it does a warm start jump back to the main segment of the monitor program.

Following the execution of the INIT routine, the monitor program will always check the external keyboard for its inputs. Only the reset key on the keypad is

still active at this point. To restore full control to the onboard keypad, one needs only to push reset or execute a jump to location 8B4A which is the beginning of the power-on reset routine (simulates pushing the reset switch).

Now that we've used INIT, let's see what functions we have and how to access them. To begin with, there are two routines in Figure 1 referred to by the INIT program:

GKEY, the equivalent of SYM GETKEY, and
 KSTAT, the equivalent of SYM KYSTAT.

Both routines affect the same registers (A,F) and have the same overall effect as noted in the SYM manual, page 9-3.

The KSTAT routine reads the input port addressed as A801, then left-shifts the input byte. If there is an input there, the carry bit will be set. Therefore KSTAT, as a subroutine, performs exactly the same function of KYSTAT.

The ASCII keyboard is connected with its 7 output bits on port A bits 2PA6-

2PA0. Port 2PA7 is used for a key strobe input (any key down). The keyboard parity bit, if any, is not used in this application. If no key is down, the input port will be read as all zeros. If any key is down, the most significant bit of the input port will be a one due to the presence of the keystrobe bit, allowing a single left shift to set the carry bit.

The GKEY routine performs the same function as GETKEY in that it scans the display while waiting for a key to be pressed. In the process of waiting for a keystroke, the scanning of the display is controlled through the display scanning vector. This allows the user to make use of the oscilloscope output routine with only minor modifications, substituting a JSR to GKEY for the JSR to GETKEY.

All other specifications mentioned in the Synertek manual for the oscilloscope driver routine will then be valid. As a matter of fact, access to an oscilloscope and the use of the driver routine could temporarily satisfy a person's desire for a video display, at least until some suitable alternative could be found.

The ASCII keyboard scanning routine GKEY handles the keybounce problem by going into a small wait loop immediately after sensing that a key is down, then scans the display while it waits for the key to be released. After release, it interprets the original keystroke contents by stripping off the keystroke bit and returning to the calling program with the ASCII equivalent of the key in the accumulator.

Now that we've seen how the routines provide for the communication with the new keyboard, lets see how we can access all of the SYM monitor functions without resorting to the use of the keypad.

Because of the direct relation of the ASCII equivalents, the following control functions are directly accessible:

Memory: M	Jump: J
Verify: V	Execute: E
Block move: B	Go: G
Write protect: W	Calculate: C
Register: R	Fill: F
Deposit: D	

20 88 81	GKEY	JSR	SAVER	SAVE REGISTERS
AD 01 A8		LDA	A801	GET PARALLEL ASCII
F0 24		SEQ	DISP	UNLESS NONE, THEN BRANCH
85 F1		STA	00F1	STORE IT A WHILE
A9 10		LDA	#310	DEBOUNCE CONSTANT
85 EF		STA	00EF	DEBOUNCE
C6 F0	WAIT1	DEC	00F0	SMALL LOOP
D0 FC		BNE	WAIT1	
C6 EF		DEC	00EF	LARGE LOOP
D0 FE		BNE	WAIT1	
20 03 89	SCANA	JSR	IJSCNV	SCAN DISPLAY (USE SCANVEC)
2C 01 A8		BIT	A801	IS KEY STILL DOWN?
30 FE		BMI	SCANA	WAIT FOR KEY RELEASE
A5 F1		LDA	00F1	KEY UP, PROCESS KEY
29 7F		AND	#37F	STRIP KEY STROBE BIT
A5 F1		JSR	OUTCHR	SEND INTO DISBUF
A5 F1		LDA	00F1	GET IT AGAIN
29 7F		AND	#37F	STRIP IT AGAIN
4C 88 81		JMP	RESXAF	RETURN WITH ASCII IN A
A9 10	WAIT2	LDA	#310	IF NO KEY,
85 EF		STA	00EF	SCAN DISPLAY
20 03 89	SCANB	JSR	IJSCNV	THRU SCANVEC
C6 EF		DEC	00EF	A NUMBER OF TIMES
D0 F9		BNE	SCANB	THEN GO BACK
F0 CA		SEQ	GKEY	AND LOOK AGAIN
AD 01 A8	KSTAT	LDA	A801	READ ASCII INPUT
0A		ASLA		SHIFT MSB INTO CARRY
60		RTS		RET, CFLAG=1 IF KEY DN.
20 86 8B	INIT	JSR	ACCESS	UNPROTECT SYSRAM
A9 00		LDA	#00	MODIFY
8D 61 A6		STA	A661	KEYBOARD
A9 02		LDA	#02	INPUT
8D 62 A6		STA	A662	VECTOR
A9 40		LDA	#340	MODIFY
8D 67 A6		STA	A667	KEYPRESS
A9 02		LDA	#02	STATUS
8D 68 A6		STA	A668	VECTOR
4C C3 80		JMP	WARM	WARM ENTRY, MONITOR

Figure 1: ASCII Keyboard Interface initialization and communication routines.

Likewise, again because of the direct ASCII usage by the monitor, the carriage return (CR), plus sign, minus sign, forward arrow and reverse arrow functions of the ASCII keyboard will perform the same functions as those equivalent keys on the built-in keypad.

Accessing the remainder of the monitor functions will require the use of two keys simultaneously, in the fashion of a shifted character. One of the keys is the CONTROL key often found on an ASCII keyboard. The function of this key (if your keyboard doesn't have one) is to inhibit the output of the two most significant bits of the ASCII output, in this case, to force a zero to both input lines 2PA6 and 2PA5. This can be accomplished with a single switch and one type 7408 IC as suggested in Figure 2.

The following functions are accessed by first holding down the control key, then pressing the indicated ASCII key: (control key referenced by CNTL below)

- Store Double Byte: CNTL P
- Load Paper Tape: CNTL Q
- LD1 (KIM format): CNTLR LD2 (SYM hi spd): CNTL S
- USRO: CNTL T
- USR1: CNTL U
- USR2: CNTL V
- USR3: CNTL W
- USR4: CNTL X
- USR5: CNTL Y
- USR6: CNTL Z
- USR7: CNTL (
- SAVP save paper tape: CNTL
- SAV1 (KIM format): CNTL)
- SAV2 (SYM hi spd): CNTL

As may be seen above, although certain of the keys may be different, all of the monitor functions are accessible from the external keyboard, fulfilling our objectives in adding it in the first place. Actually I have hedged a bit for a couple of items, but these items I figure are not needed on the external keyboard, but serve their purpose better on the keypad, specifically the DEBUG ON/OFF, the SHIFT, and the ASCII keypad items. DEBUG is a hardware function which can be simulated by software, so in a program we can access the function. SHIFT is a monitor translation routine, appropriate only to the placement and arrangement of the keys on the keypad. Finally, the ASCII key is not necessary externally since everything we output from the external keyboard is formatted in parallel ASCII anyway.

The SYM-1 is a very powerful single-board computer. The addition of a parallel ASCII keyboard inexpensively provides us with a basis for further expansion of the SYM-1's capabilities.

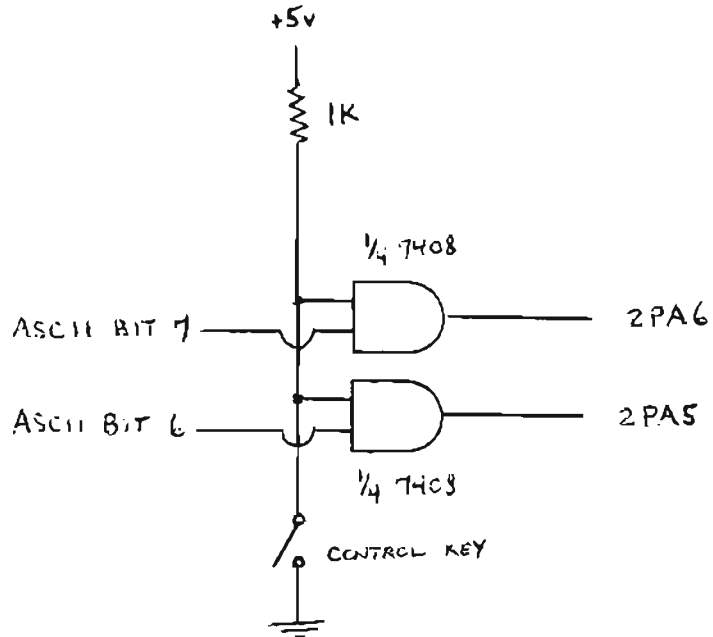


Figure 2: Adding a CONTROL key

The SY6516 PSEUDO-16 microprocessor, after power up, is identical to the 6500 series microprocessors in terms of instruction set (source code only), registers and system timing. However, due to im-

provements made in the state counter and look ahead carry in the SY6516, several of the instructions in the 6500 series will require fewer cycles to execute. Instructions in this category are:

Instruction	Addressing Mode	6500 #Cycles	6516 #Cycles
STA	(IND,Y)	6	5
	(ABS,X)	5	4
LDA	ABS,Y	4	3
INC	ABS,X	7	6
DEC	ABS,X	7	6
ASL	ABS,X	7	6
ROL	ABS,X	7	6
ROR	ABS,X	7	6
TAX	IMPLIED	2	1
TXA	IMPLIED	2	1
TAY	IMPLIED	2	1
TYA	IMPLIED	2	1
TSX	IMPLIED	2	1
TXS	IMPLIED	2	1
SEC	IMPLIED	2	1
CLC	IMPLIED	2	1
SED	IMPLIED	2	1
CLD	IMPLIED	2	1
SEI	IMPLIED	2	1
CLI	IMPLIED	2	1
CLV	IMPLIED	2	1
INX	IMPLIED	2	1
DEX	IMPLIED	2	1
DEY	IMPLIED	2	1
PLP	IMPLIED	4	3
PLA	IMPLIED	4	3
NOP	IMPLIED	2	1
RTI	IMPLIED	6	5
RTS	IMPLIED	6	4
TSX	FLAGS	N,Z	NO FLAGS
TSR	ABS	6	5

Table 1: SY6516 Pseudo-16 compatibility to SY6500 series microprocessors

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OTHELLO may be played by one or two players and is similar to chess in strategy. Once a piece has been played, its color may be reversed many times, and there are also sudden reverses of luck. You can win with a single move. Vince Corsetti's program does all the work of keeping board details and flipping pieces. **\$9.95**

SINGLE DRIVE COPY is a special utility program, written by Vince Corsetti in Integer BASIC, that will copy a diskette using only one drive. It is supplied on tape and should be loaded onto a diskette. It automatically adjusts for APPLE memory size and should be used with DOS 3.2. **\$19.95**

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HARDWARE

LIGHT PEN with seven supporting routines. The light meter takes intensity readings every fraction of a second from 0 to 588. The light graph generates a display of light intensity on the screen. The light pen connects points that have been drawn on the screen, in low or high resolution, and displays their coordinates. A special utility displays any number of points on the screen, for use in menu selection or games, and selects a point when the light pen touches it. The package includes a light pen calculator and light pen TIC TAC TOE. Neil D. Lipson's programs use artificial intelligence and are not confused by outside light. The hi-res light pen, only, requires 48K and ROM card. **\$34.95**

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A HIRES Graph-Plotting Subroutine in Integer BASIC for the APPLE II

A BASIC subroutine is presented which permits HIRES graph plotting. It includes X and Y axes generation with scale markers as well as the plotting of user specified points. This will make it easy to display the results of a variety of problems, functions, correlations, etc.

Richard Farn
36 Fifth Avenue
Singapore 10
Republic of Singapore

The article entitled APPLE II High Resolution Graphics Memory Organization, found in MICRO 7:43 by Andrew H. Eliason is of tremendous value to those who wish to plot in HIRES graphics. The following graph plotting subroutine utilizes formulae given in this article.

Referring to the listing on being called by the GOSUB 9000 statement in the main program, the subroutine first clears page 1 of HIRES graphics memory at line 9023. This is quite a time-consuming process and the impatient experimenter may care to replace this line with a CALL statement to an equivalent machine language subroutine. I have actually tried this and found that it reduces the time execution for the complete plotting routine by approximately half.

Having set the graphics and HIRES modes in line 9060, the routine then proceeds to plot the X and Y axes. Scale markers are placed at 20-point intervals along the two axes.

The final stage in the subroutine in-

volves the plotting of the points. The magnitude of these points are stored in matrix GPH which is dimensioned for 279 elements in the main program. Only values GPH(X) between 0 and 91 inclusive can be plotted.

As you may recall, the display area of HIRES graphics is a matrix comprised of 280 horizontal by 192 vertical points. The subroutine fetches elements of GPH, does the necessary calculations, and outputs the results on the screen. To prevent the disfigurement of the two axes, I have avoided the plotting of points less than one byte away from the Y-axis and on the X-axis itself.

For successful application of this graph plotting subroutine, observe the following rules:

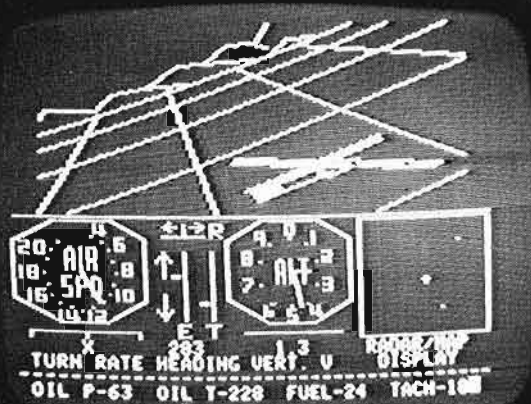
- a) Only an APPLE II with a minimum of 16K bytes of memory can be used
- b) Ensure that the main program contains the statement DIM GPH(279).
- c) Only values of GPH(X) such that $0 < GPH(X) < 191$ where X ranges from 0 to 279, inclusive, will be plotted.
- d) Set HIMEM:8191 to restrain intrusion into page 1 of HIRES graphics memory.

Here are two short programs demonstrating the performance of the high resolution graphics-plotting subroutine.

```
10 DIM GPH(279)
20 FOR I=0 TO 279
30 GPH(I) = RND(191)
40 NEXT I
50 GOSUB 9000
60 END
```

```
10 DIM GPH(279)
20 FOR I=0 TO 279
30 GPH(I) = I/2 - 30
40 NEXT I
50 GOSUB 9000
```

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```

LIST
9000 REM *
9001 REM * HIRES GRAPH-PLOTTING
9002 REM * SUBROUTINE
9003 REM *
9004 REM * BY R.S.K. FAM
9005 REM * 26/4/79
9006 REM *
9007 REM * DATA IS STORED IN GPH(X)
9008 REM * CONSISTIJG OF 200 POINTS
9009 REM * 0 <= GPH(X) <=191
9010 REM *
9011 REM * SET HIMEM:8191
9012 REM *
9020 REM *
9021 REM * CLEAR SCREEN
9022 REM *
9023 FOR I=8192 TO 16383: POKE I,
      0: NEXT I
9030 REM *
9040 REM * SET HIRES MODE
9050 REM *
9060 POKE -16304,0: POKE -16297,
      0: POKE -16302,0
9140 REM *
9150 REM * PLOT Y-AXIS
9160 REM *
9170 FOR LV=0 TO 191:PT=1: IF (LV+
      9) MOD 20=0 THEN PT=7: POKE
      (LV MOD 8*1024+(LV/8) MOD 8
      *128+(LV/64)*40+8192),PT: NEXT
      LV
9200 REM *
9210 REM * PLOT X-AXIS
9220 REM *
9230 PT=0: FOR LH=0 TO 279: IF LH MOD
      20<>0 THEN 9240:PT=PT+1: FOR
      MK=1 TO 2: POKE LH/7+16336-
      (1024*MK),64/(2 ((PT+5) MOD
      7)): NEXT MK: GOTO 9242
9240 POKE LH/7+16336,255
9242 NEXT LH
9260 REM *
9270 REM * PLOT POINTS
9280 REM *
9290 FOR LH=8 TO 279:LV=191-GPH(
      LH): IF LV<0 OR LV>=191 THEN
      9330
9310 BV=LV MOD 8*1024+(LV/8) MOD
      8*128+(LV/64)*40+8192: POKE
      LH/7+B*V,2 (LH MOD 7)
9330 NEXT LH: RETURN

```

MICRO — 80

Not to worry! The title of this editorial does not mean that MICRO is going to start covering TRS-80, 8080, or any other processor. MICRO is "The 6502 Journal" and has no plans to change that. The title simply refers to 1980 and/or the 1980's. Writing this at the start of a new decade, I want to reflect on what MICRO accomplished in the 70's and describe some of its plans for the 80's.

MICRO in the 70's

MICRO was started in 1977 to fill two needs:

1. Provide a quality magazine devoted to the 6502 microprocessor and the various microcomputers based on the 6502. At that time, very little was being printed about the 6502 in the major journals.
2. Provide a means for 6502 oriented dealers and manufacturers to economically reach their specific 6502 audience.

The first issue was printed at a "store front" print shop, ran 28 pages, and had an immediate circulation of 450 copies. Since then MICRO has grown in many ways. It is now printed at a commercial printer, is 68 pages or more, has an immediate circulation of almost 10,000 copies, is completely typeset, and is published monthly.

MICRO decided from the start to pay its authors for their material. In fact, we pay twice! Authors received \$25.00 per page for material in the magazine, and then received an equal amount for material reprinted in "The BEST of MICRO".

MICRO in the 80's

In the 1980's, we will continue to provide serious articles on 6502 systems, to maintain the Software Catalog, and to continue the on-going 6502 Bibliography. With our monthly format and three week printing/ mailing schedule, we will continue to print the most current advertisements.

A number of features will be added. These will include regular "news" columns about each of the major microcomputers; "topical" columns about the use of the 6502 in business, medicine, process control, education, etc.; the MICROscope in which qualified reviewers present detailed hardware/software product reviews; a "6502 Club Forum" highlighting club activities; and many other useful features.

To make writing for MICRO even more profitable, a new author payment schedule has been established. Authors will now receive up to \$50.00 per page for articles as well as residual payments for reprints. The minimum amount per page will be \$25.00, with the actual amount dependent on the type of material, quality of the article, etcetera.

I welcome any suggestions you have for improving MICRO, and hope that you will continue to participate in the exciting, expanding 6502 world, not just as a MICRO reader, but as an active contributor.

Robert M. Trigg

Writing for MICRO

Writing for MICRO is probably easier than you think, and more rewarding too! In this rapidly expanding world of 6502 microcomputers, no single person knows everything, and no single person knows nothing. Every computerist has something to contribute.

MICRO Pays Well

Even though MICRO is much smaller than Kilobaud, Byte, and the other major general microcomputing journals, it pays its authors as much or more than the others in general. Byte, for example, has a published scale of \$25 to \$50 per page. MICRO pays the same rates. Beyond that, MICRO pays its authors when articles are reprinted in "The BEST of MICRO". This means that a first rate article can earn its author up to \$100 per page. If you stop to consider that it normally takes at least three or four pages to present an idea, a discussion and a program, you will realize that it adds up.

MICRO Is Read By 6502 Computerists

Since MICRO is totally devoted to the 6502, its readership is composed only of computerists interested in the 6502. Since the general journals cover many different processors, a 6502 article will only appeal to a fraction of the readers, and may easily get lost between TRS-80 junk. An article you write for MICRO will get out to the right people.

MICRO Has Many Opportunities

There are many different ways you can write for MICRO. Each of the ways has its own merit and may apply to you at different times on different topics:

LETTERS and COMMENTS: If you have an observation, suggestion, hint, or other small item of interest

which you think others should know about, a 'Letter to the Editor' can be the perfect vehicle. MICRO does not pay for this type of contribution, but you will get full credit with a byline. Small notes about the AIM, SYM, or KIM may be included in "ASK the Doctor", again without payment but with a byline. It doesn't take long to jot down your information and send it in. And, in addition to getting your material in print, you may be really helping other 6502 computerists.

ARTICLES: When you have a larger idea, a complete article is appropriate. While it does take some time and effort on your part to put your information into a form that can be understood by others, it is probably not as difficult as you imagine. The MICRO Staff will work with you to get the article into its final form. You do get paid for any article which is published. While you may never get rich writing articles, you can easily earn enough for that extra memory or whatever.

COLUMNS: We are now actively seeking a few highly qualified individuals to write regular columns. We plan to have a column every other month or so on each of the major 6502 microcomputers, covering news of new products, events, and other items of interest. We also plan to feature regular columns on the use of the 6502 in various fields such as Medicine, Education, Business, Process Control, etcetera, and are looking for writers in these areas. If you are in a position to really know what is happening on one of the 6502 microcomputers or in one of the major application areas, contact us. MICRO will be paying the highest rates for these columns.

MICRO Opportunities

There are numerous opportunities for anyone who wishes to participate in MICRO. We have a Writer's Guide available which will show you in detail how to submit an article to MICRO. Please check the box on the tear-out form in this issue and send it in. MICRO will do the rest.



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STEP OFF APPEND DUMP FIND

```
LIST
10 GOSUB 99
15 PRINT I
16 GOTO 10
99 INPUT J
100 IF J = 0 THEN END
200 I = SQR(J):RETURN
READY

RENUMBER 100,10

READY.
LIST

100 GOSUB 130
110 PRINT I
120 GOTO 100
130 INPUT J
140 IF J = 0 THEN END
150 I = SQR(J):RETURN
READY.
```

```
RUN
?DIVISION BY ZERO ERROR IN 500
READY.
HELP
500 J = SQR(A*B/J)
READY
```

```
RUN
READY.
DUMP
A1 = 10
BW = -6.1
CS = "HI"
READY.
```

```
APPEND "INPUT"
PRESS PLAY ON TAPE #1
OK
SEARCHING FOR INPUT
FOUND INPUT
APPENDING
READY.
```

```
TRACE #100
#110
#150
#190
#175
#200
READY.
RUN
ENTER YOUR NAME? JIM
HI JIM.
HOW OLD ARE YOU?
```

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Multiplexing PET's User Port

What do you do when you need to Input or Output more bits of data than your micro can handle? You multiplex! This is not very difficult with a little special hardware and very simple program. This implementation is on a PET, but can be used on any system.

E.D. Morris, Jr.
3200 Washington
Midland, MI 48640

Part of my duties as a chemist involve taking readings from an analytical instrument. The data consists of a series of six digit numbers. These are dutifully copied down on paper and later key-punched into a large computer. The calculations could easily be done in BASIC on a personal computer if there were some way to automatically get the data into the computer.

The data is presented on the front panel as six 7-segment LED readouts. However, the rear panel supplies the data in BCD (Binary Coded Decimal) format. Each decimal digit is represented by four binary bits. Numbers above 9 (binary 1001) are not allowed. For six decimal digits a total of 24 bits is required. Unfortunately most small personal computers such as the PET have only an 8-bit I/O port.

The solution is to multiplex, or combine the data into fewer input lines. For example, each decimal digit has a 1,2,4, and 8 bit. These 24 bits of data could be wired through a 6-position, 4-pole switch to produce four outputs. The computer could then read one digit at a time, change the position of the switch and read again until all six digits are read. The decimal number must then be reconstructed by multiplying each digit by 1, 10, 100, etc., and summing the results.

A mechanical 6-position switch is not really practical for computer operation, but the electronic analog exists in the 74LS151 integrated circuit. The 74LS151 is known as a 1-of-8 data selector and acts like an 8-position single pole switch. This chip has eight inputs (pins 1,2,3,4,12,13,14,15) and one output (pin 5). Three additional pins (9,10,11) control which of the inputs is connected to the output.

If four 74LS151's are used, we have an 8-position, 4-pole switch. The 1's bits from all the decimal digits are connected to one data selector. All of the 2's bits are connected to a second data selector, etc. The output from the four integrated circuits are connected to the four lowest bits (D0 D1 D2 D3) on the PET input port. The next three bits of the I/O are set to outputs (D4 D5 D6) and used to control the 1-of-8 data selectors. Since I wasn't sure how much current the PET output could supply, I used a 74LS04 hex buffer between the PET outputs and the data selector control lines. The highest bit (D7) is used as a flag in my application to signal the computer that a number needs to be read.

Figure 1 gives a schematic drawing of the circuit. For clarity, the +5 volt connection (pin 16) and ground connection (pins 7 and 8) are not shown on the data selectors. I built this circuit on a 3" x 4" perf board which plugs directly in-

to the PET user port. If low power logic is used, the circuit requires 5 volts at 20ma. This could be taken from the PET second cassette port. Since Commodore warns against this, I added a 5 volt regulator to my board and stole unregulated 9 volts from the computer. Before plugging this circuit into your computer, you should power it up with an external supply and verify that each input works when tested with a voltmeter.

The following program will allow the PET to read a 6-digit decimal number through the user port.

```
10 POKE 59459,112
20 A = 59471
30 FOR I = 0 TO 5
40 P = I*16
50 POKE A,P
60 B(I) = PEEK(A)AND15
70 NEXT I
80 C = B(0) + 10*B(1) + 100*B(2) +
+ 1000*B(3) + 10000*B(4)
+ 100000*B(5)
90 PRINT C
```

Explanation of Program

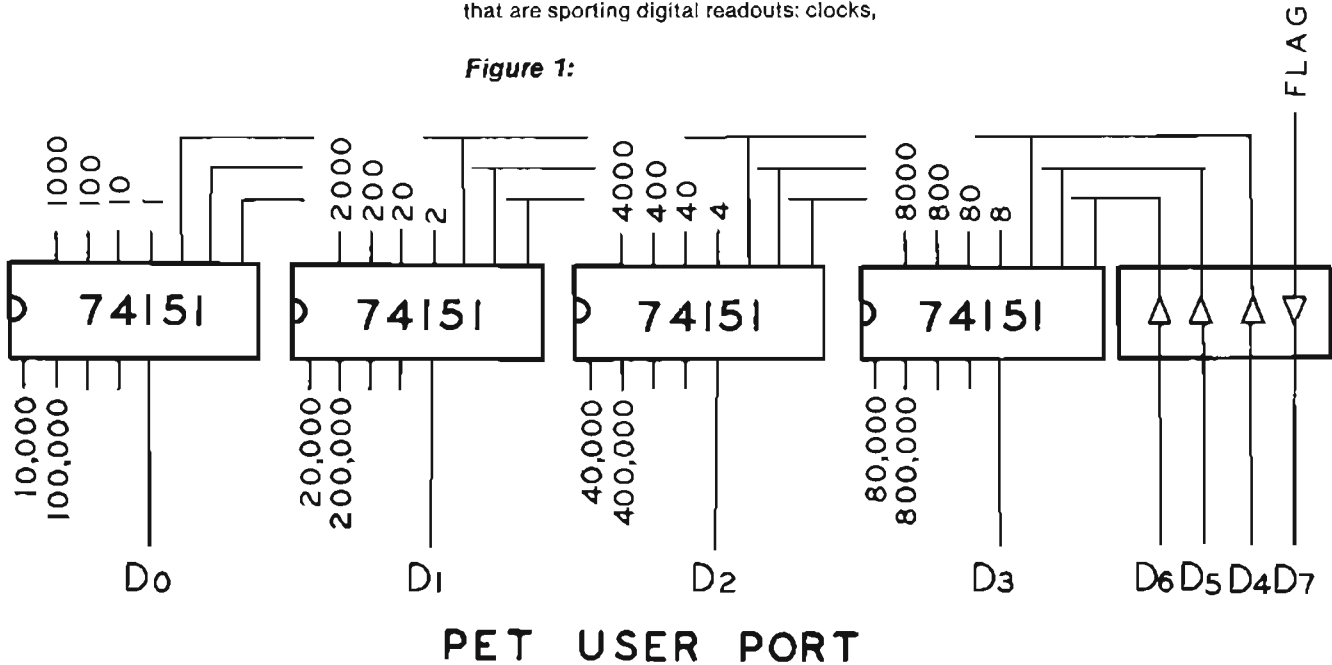
Line 10 Sets up D4 D5 and D6 as outputs
 Line 20 User Port address
 Line 50 Sends signal to data selectors
 Line 60 Reads lower four bits & masks out others
 Line 80 Reconstructs decimal number from digits
 Line 30 If I goes from 0 to 7, then all 32 bits are read.

I am using only 24 bits, however, the circuit described here will read up to 32 bits through an 8-bit I/O port. If you don't need D7 for a flag, you can use the 74LS150 1-of-16 data selector to read 64 bits. D7 would then be a fourth control line.

You probably don't have an analytical instrument around the house to keep track of, but look at all the other devices that are sporting digital readouts: clocks,

timers, scanners, thermometers, TV channel selectors, etc. The data for these is normally generated in BCD format and then converted to 7-segment for display. A multiplexing technique can be used whenever you have more bits of data than input ports. The bits don't have to be a decimal number; each bit could represent of sensor of a burglar alarm system or the position of a turnout in a model train layout.

Figure 1:



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The Binary Sort

Here is a concise description of the Binary Sort concept, and a detailed implementation in BASIC that should be easy to adapt to any micro or application.

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Sometimes we have an array of data which we need to search in order to find the location of one particular element in it. This is more common with alphabetic data, but we may have to do it with either alpha or numeric data. The simplest way to find the item is to use a FOR-loop, checking each item individually until we find the one we are looking for. The average number of steps through the loop that must be made to find a given item is approximately half the length of the list. If the item is not on the list, then the program must execute as many steps through as there are items on the list. When the array is short, there is no problem. However, as the array gets longer, this method becomes more and more inefficient. An array that has 500 elements in it will require an average of 250 steps through the loop to find an item. Such a search will take several seconds.

When the list is ordered (i.e., sorted into either ascending or descending order), there is a much more efficient way to search the list: the binary search. Basically stated, in a binary search you continually divide the list into two halves and then eliminate the half which cannot contain your item. (Because the list is always divided into two halves, this is called a binary search.) For example, if the item at the half-way point is larger than the item you are looking for, you know that your item cannot be in the second half of the list. So, you eliminate it from consideration. You then divide the remaining list in half, and continue the process of eliminating and dividing until you find the item, or until you cannot cut in half any more. If that happens, the item you are looking for is not on the list, and your search has failed.

In a FOR-loop search, each step through the loop eliminates only one item from the list; in a binary search, each step through eliminates half of the remaining list. Taking as an example a list of 255 items, Table 1 shows how much is eliminated at each iteration through the loop. The first column is the step number, the second column gives how many were eliminated in that step, and the third tells the total number of items now eliminated.

After step 8 through the search, you have either found your item (and you may well have found it before step 8), or your search has failed. At any rate, it took you only 8 times through the loop to find your item, as opposed to the average of 128

(maximum: 255) that a straight search would require. The best part is that if you double the list, the binary search requires only one more step through the loop; double it again, and add just one more time through! Obviously, this is a wonderful tool.

There are only two requirements for a binary search: 1) the list must be in order; and 2) the items on the list must be unique (or, if not, it doesn't matter to you which of the duplicated items is located).

To do a binary search, we need two variables. One to point at where we are in the array, and one to keep cutting the search-field in half. In Table 2, I call them

Step No.	Eliminated this step	Total eliminated
1	128	128
2	64	192
3	32	224
4	16	240
5	8	248
6	4	252
7	2	254
8	1	255

Table 1.

Step	PT	IV	Find?	New IV	+ or -	New PT
1	8	8	no	4	+	12
2	12	4	no	2	-	10
3	10	2	no	1	+	11
4	11	1	YES!			

Table 2.

PT (for "pointer") and IV (for "interval"). IV will get cut in half each time through, until it gets down to 1. IV will be added to PT if we have to go further down the list; it will be subtracted from PT if we have to come back up higher on the list. To illustrate this, let us assume an array of 15 elements. The item we are searching for happens to be in position 11. Let's step through and see what happens to PT and IV at each step.

The logic to do this is not difficult. Let's say that our array is called L1\$, and is an alpha array sorted into ascending (i.e., alphabetical) order. We have another variable TL ("total" — it is the same variable we would have used in a FOR-loop: FOR I 1 TO TL) which tells us how many items are currently in the array. Finally, the item we are trying to find is stored in the variable SW\$. The simple algorithm appears in Figure 1.

If the array were sorted into descending order, the " " and " " symbols in statements 40 and 50 would be reversed. Notice that we use the INT function and round up. This is the equivalent to the CEILING function. Both things are necessary; if you don't round up, you won't be able to get to the end of the list, and non-integers will get clobbered during the division process.

As it happens, I do not like the redundancy of lines 40 and 50; I prefer to make them a little more efficient. I do it so that IV is always added to PT. Then, with one compare, I find out if IV should be positive (so that the addition will add IV to PT) or negative (so that the addition will, in effect, subtract IV from PT). So, I prefer to have lines 40 and 50 as follows:

```
40 IF L1$(PT) SW$ THEN IV=-IV
```

```
50 PT = PT + IV
```

While this is certainly more "elegant," it also adds a problem. IV will quite often turn out negative, and that will really foul up what happens in statement 30. So, we have to change 30 to:

```
30 IV = INT((ABS(IV))/2 + .5)
```

```
10 PT=INT(TL/2+.5): IV=PT
```

```
20 IF L1$(PT)=SW$ THEN GOTO [you have found it!]
```

```
30 IV=INT((ABS(IV))/2+.5)
```

```
40 IF L1$(PT) SW$ THEN IV=IV-PT
```

```
50 IF L1$(PT) SW$ THEN IV=IV+PT
```

```
60 GO TO 20
```

Figure 1.

Now, having added the ABS function into line 30 to ensure that IV will always be positive, I am not sure that I have gained anything in efficiency. But, I think that it is more elegant, so I'll leave it!

If you try to run the program the way it is, you may have a problem: if the item that you are searching for is not on the list, you will get into an infinite loop and the only way out of the algorithm is to find the item. So, we have to check to see if IV has the value of 1. If it does we cannot cut in half any more; we cannot search any more. We need to test IV's absolute value, and I put it right after the compare, calling it line 25.

```
25 IF ABS(IV)=1 THEN GOTO [the search has failed]
```

If everything in the world were perfect, that would be the algorithm. However, since consistently rounding IV up for the reasons pointed out above, we may actually, at some times, exceed the bounds of the array, raising the error condition. There are several different ways to handle the problem; I believe the easiest is to take the value of IV away from PT and continue on from there. Since I don't know at this point if IV is negative or positive, I simply change its sign and add it to PT in line 55.

```
55 IF PT TL OR PT 1 THEN IV=-IV: PT=PT+IV
```

(If you really don't like to have IV go negative and then to have to use ABS, you can use the original version of lines 40 and 50, and then use two statements here in place of 55.

```
IF PT 1 THEN PT = PT + IV
and IF PT TL THEN PT = PT - IV)
```

My version of the binary sort algorithm is shown in Figure 2.

There is, unfortunately, still one more potential problem. If the number of items in the array (TL) is exactly a power of 2 (16, 32, 64, 128, etc.), the search will not locate the very last item in the array. The reason is that when you cut in half, you don't cut perfectly in half. If the array has 16 elements in it, you look first at element 8: there are actually 7 elements above it in the array; but there are 8 elements below it! If the array has any number other than a power of 2, there is always one division which has to be rounded up, and that rounding up gives us room to get to the very end of the array. (Actually, it also caused the problem of going beyond the bounds of the array, which made us add line 55.) There are several ways to overcome the problem, including preventing the array ever from having an "undesirable" number of items. For me, the simplest thing to do is to

```
10 PT=INT(TL/2+.5): IV=PT
```

```
20 IF L1$(PT)=SW$ THEN GOTO [found it! PT is the number of the item]
```

```
25 IF ABS(IV)=1 THEN GOTO [the search has apparently failed]
```

```
30 IV=(INT((ABS(IV))/2+.5))
```

```
40 IF L1$(PT) SW$ THEN IV=-IV
```

```
50 PT=PT+IV
```

```
55 IF PT TL OR PT 1 THEN IV=-IV: PT=PT+IV
```

```
60 GOTO 20
```

Figure 2

check the last item in the array if the search fails. If they don't match, then the search actually has failed. But if it does succeed at this point, I do have to assign the value of TL to PT, as PT is what is carried into the main program to tell what item number was found. I do the entire thing in line 70:

```
70 IF SW$=L1$(TL) THEN PT=TL: GOTO [found it!]
```

I also have to change line 25, so that the GOTO there branches to 70.

If the compare in line 70 yields a false, then the search has really failed, and you drop out of the binary search algorithm. Let's now look at the complete algorithm in Figure 3, which is missing only the line numbers after the GOTO statements which will link the search to the programs you use it in.

```
10 PT=INT(TL/2+.5): IV=PT
```

```
20 IF L1$(PT)=SW$ THEN GOTO [found it]
```

```
25 IF ABS(IV)=1 THEN GOTO 70
```

```
30 IV=INT((ABS(IV))/2+.5)
```

```
40 IF L1$(PT) SW$ THEN IV=-IV
```

```
50 PT=PT+IV
```

```
55 IF PT TL OR PT 1 THEN IV=-IV: PT=PT+IV
```

```
60 GOTO 20
```

```
70 IF SW$=L1$(TL) THEN PT=TL: GOTO [found it]
```

```
80 REM Search has failed and you're out of the binary search algorithm.
```

Figure 3

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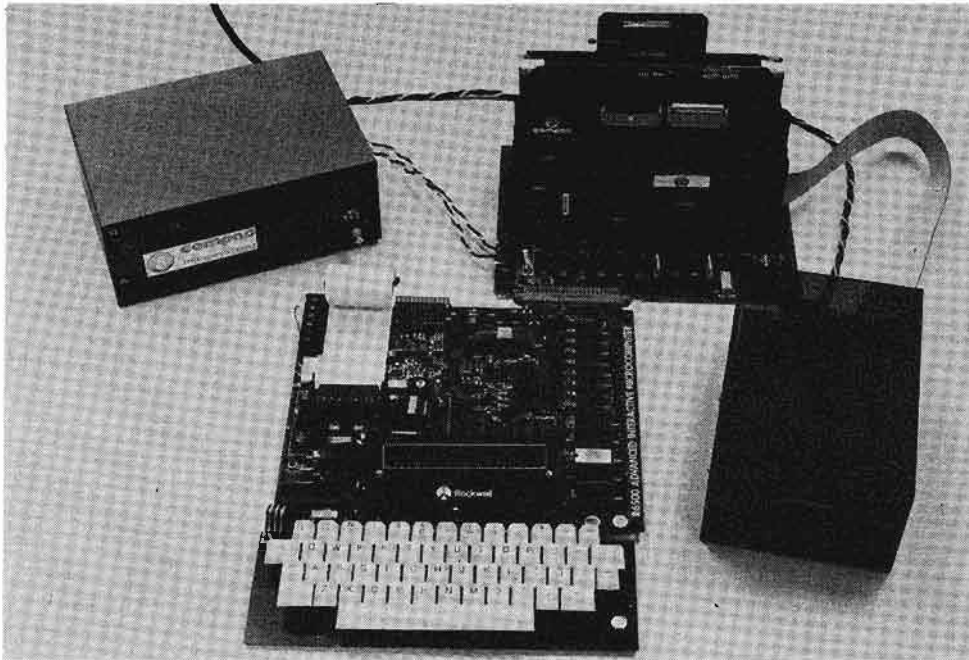
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A Complete Morse Code Send/Receive Package for the AIM 65

Here is a valuable program for any AIM user. While it will be of most interest to a HAM radio buff, the techniques which include the use of timers, interrupts, table lookups, and so forth should be instructive to everyone.

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I. FEATURES:

- A . Send Morse code using the AIM 65 keyboard. A 256 character buffer permits typing ahead.
 - B . Send pre-loaded Morse code messages. Three messages totaling 256 characters can be sent.
 - C . A simple interface circuit allows the program to operate as an electronic keyer.
 - D . Code speed in words per minute is entered on the keyboard and displayed on the AIM 65 display
 - E . Control of the entire program is from the keyboard.
 - F . A single integrated circuit provides the interface for receiving Morse code.
 - G . The received code is converted to alphanumeric characters on the AIM 65 display, and is scrolled left as the code is received.
 - H . Code speed is adjustable from 5 to 99 wpm.
- A . Load the program given in the listings and construct the interface circuits shown in Figures 1 and 2. The cross-coupled NAND gate interface in Figure 1 is not needed if you do not operate the program as a paddle-type electronic keyer. Set the P register to zero before starting the program.
 - B . Execution begins at address \$0500. After initializing the program, three messages (called A, B and C) may be entered from the AIM 65 keyboard. As messages are entered they will appear on the display, and they will be recorded by the thermal printer if the printer is on. If a mistake is made, pressing the DEL key will clear the character and a new character may be entered. The RETURN key is pressed when a message is complete. An example of a message is " CQ CQ CQ DE KOEI KOEI K." Message A is the first one entered, message C is the last. The sum of the characters including spaces cannot exceed 255. Pressing the RETURN key at the end of the third message causes the program to proceed to the keyboard-send mode. If you do not have any messages to place in memory, hit the space bar and the RETURN key three times in succession to enter the keyboard-send mode.
 - C . In the keyboard-send mode, pressing a key will cause the corresponding Morse character to be sent, while pressing a control key will cause the corresponding control operation (described below) to be carried out. The keyer will also operate at this time if you wish to use the keyer rather than the keyboard.
 - D . The first thing you will want to do in the keyboard-send mode is set the code speed. Press the CTRL key; and, while holding down the CTRL key, press the S key (S is for "speed"). Release these keys and then enter the code speed at which you wish to operate. The two-digit decimal number should appear at the far left of the display.
 - E . Pressing CTRL A, B, or C will cause the corresponding message to be sent. Any set of spaces in any of the messages may be interrupted by the keyer (to fill in an RST report, for example), but they will not be interrupted by keyboard entries other than control functions.
 - F . Morse code may be sent from the keyboard by typing the characters. They appear on the display as they are typed, and they disappear from

II. OPERATING INSTRUCTIONS

The following paragraphs serve as an operating guide for the program.

the display when they are sent. You can type ahead of the Morse code being sent by filling a 256 character buffer. (No warning is given for a full buffer because, in my experience, you rarely get 256 characters ahead.) If while sending Morse code with the keyboard you find that you have made a mistake, perish the thought, a delete function has thoughtfully been provided. Use the DEL key to try to get to the mistake before the send program gets to the character (this can be challenging at high code speeds or with slow fingers). Also, if you delete when there are no characters left to delete, you will get the contents of the entire buffer. Hit the RETURN key if this happens. RETURN starts the entire program over.

G. The RETURN key serves as a panic button. It will restart the program when you are in the keyboard-mode. It can get you out of desperate situations. The RETURN key followed by the F1 key puts you right back in the keyboard-send mode without affecting the messages A, B, and C.

H. The speed can be changed at any time, even in the middle of a message or when the send buffer has characters left to be sent. However, the CTRL S interrupts the program until the two-digit number is entered; so if you are in the middle of a dot or dash, the transmitter will remain on until you finish entering the speed. At that time the code element, the character, and the remaining message will be sent at the new speed.

I. If you wish to preload the buffer while the "other guy" is sending, you can press CTRL L (L is for "load"). The program loops while you load the buffer.

J. CTRL K returns the program from the load loop (or the receive mode) to start sending the code in the buffer. CTRL K always sends the program back to the keyboard-send mode, disabling the CTRL L mode and the receive mode.

K. CTRL R sends the program to receive code. The program will copy code over a wide range of code speeds, so adjustments in the code speed are infrequent. However, if you want to be "right on," the left-most digit of the speed display will blink if your speed is too fast, while the right-most digit will blink if your speed is too slow. Blinking digits are produced by measuring the incoming dot length. Variations in the dot length of the incoming code may cause both digits to blink. Then you are "right on!" Noise spikes are typically regarded as excessively short dots and will cause the left-most digit to blink.

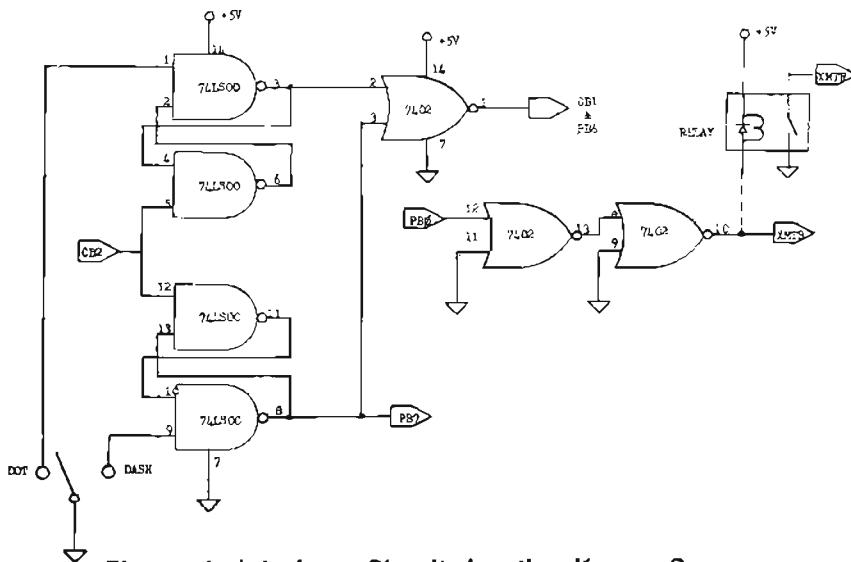


Figure 1: Interface Circuit for the Keyer. Some transmitters will require a relay for keying. This interface circuit may be omitted if you do not wish to operate in the keyer mode.

L. Do not spend a lot of time trying to zero-in on someone's code speed. The finite resolution of the speed settings prevent a measurement that is more accurate than about 2 wpm. Variations in the weight ratio and other personal characteristics of sending will also affect the actual speed. The code-speed measurement will be accurate for machine-sent code, from W1AW or another AIM 65 for example. The received code will appear on the AIM 65 display moving from right to left. A too-high speed setting is better than too low.

M. The bandwidth of the interface circuit,

an LM567 tone decoder, is narrow, so tuning is delicate. Watch the LED output carefully until it blinks in synchronism with the incoming code. Practice copying W1AW broadcasts until you become familiar with the operating of the receive mode. Remember that an AIM 65 and an LM567 are somewhat less powerful than the human mind and the ear when copying faint signals in the presence of noise.

N. You can return from the receive mode to the keyboard-send mode by the CTRL K operation.

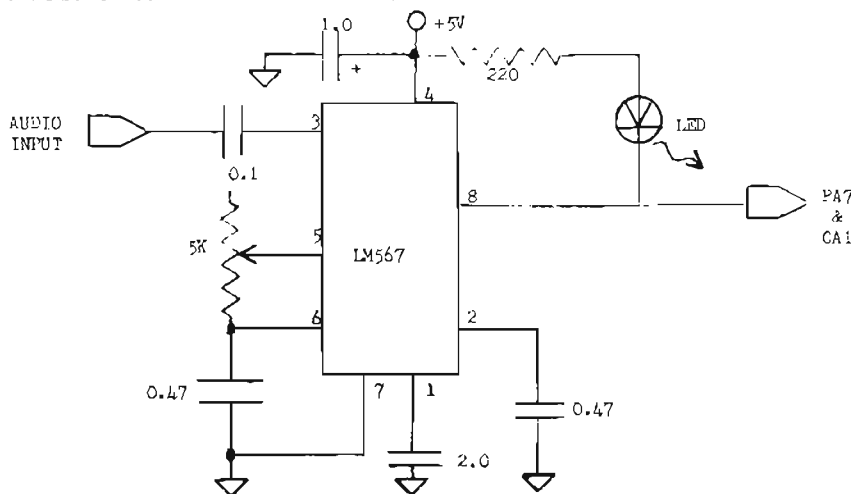


Figure 2: Interface Circuit for the Receive Mode. The 5K potentiometer is adjusted to correspond to the center frequency of the CW note. The signal is tuned with the receiver until the LED flashes in unison with the code being received.

TABLE I. Routine Location Table.

<u>LOCATIONS</u>	<u>FUNCTION</u>
\$0200 - \$02FF	- Messages A, B, and C are stored in these locations.
\$0300 - \$03FF	- Keyboard buffer. Holds up to 256 characters so you can type ahead.
\$0420 - \$045C	- ASCII to Morse Code Conversion Table
\$0480 - \$04D7	- Morse Code to ASCII Conversion Table
\$04F3	- Conversion of comma (,) in Morse Code to ASCII.
\$0500 - \$0564	- Routine to initialize certain registers and input the three messages with the keyboard.
\$0565 - \$0582	- Set up interrupt vector and start servicing the keyboard on an interrupt basis.
\$0583 - \$058E	- Initialize the keyboard buffer memory locations.
\$058F - \$05A2 and \$05F4 - \$05F9	- Keyboard wait loop. Program waits here until a keyboard entry has been made to the buffer. When such an entry is made, the program sends the character.
\$05A3 - \$05F3	- Subroutine SEND. Contains subroutine DOT at \$05CB, subroutine DASH at \$05E4, and subroutine TIMER at \$05F9.
\$0600 - \$065F and \$09A7 - \$09C7	- Subroutine KEYBOARD. This subroutine is part of the interrupt routine that scans the keyboard. If a key has been depressed, it stores the ASCII character in the buffer, unless it is a control character. If it is a control character, the appropriate control function is implemented. For example, Control R sends the program to the receive routine.
\$0660 - \$0671	- Subroutine DISPLAY. Used to display characters on the AIM 65 display.
\$0672 - \$0684	- Subroutine MODIFY. Used to shift the elements in the display buffer to the left.
\$0685 - \$069A	- Subroutine BACKSPACE. Used to shift the elements in the display buffer to the right, entering a blank (space) for a deleted character.
\$069B - \$06A5	- Subroutine CLEAR. Used to clear the display buffer.
\$06A6 - \$06BF	- Subroutine NONAME. Used to clear the display location that contained the character just converted to Morse code.
\$06C0 - \$06E5	- Interrupt routine for keyer.
\$06E6 - \$06ED and \$0904 - \$09A6	- Interrupt routine to scan the keyboard.

III. BACKGROUND

Morse code send/receive programs have appeared in several forms in the literature. Consult the bibliography for some useful references. The routines used in this program have previously been described by the author's articles in MICRO (MICRO is published by MICRO INK, Inc., P.O. Box 6502, Chelmsford, MA 01824), and will not be described in detail here. Table 1 locates the various routines, and the references given in the bibliography will explain most of these routines.

The keyboard is read on an interrupt basis, making extensive use of the monitor subroutine ONEKEY at \$ED05. Also, the keyboard-read routine duplicates the monitor subroutine GETKEY at \$EC40, with some important modifications for interrupt operation. The T1 timer on the user 6522 is used to produce interrupts every \$8000 microseconds, at which time the keyboard is scanned.

The Morse code receive algorithm may be summarized as follows: Define the presence of a tone as a *mark* and the absence of a tone as a *space*. The receive program idles in a loop until the leading edge of a mark element produces an interrupt request (IRQ). At that time, a mark-counter memory location is incremented at 1024 microsecond intervals until the mark is gone. During a space a space-counter memory location is incremented. When the space-counter is equal to 1/2 the dot length as determined by the speed setting, then the mark-counter memory location is examined to determine if the mark was a noise pulse, a dot, or a dash. If the mark counter was less than 1/2 the dot length, the mark is regarded as a noise pulse. If the mark counter is between 1/2 the dot length and twice the dot length, the mark is regarded as a dot. If the mark counter exceeds twice the dot length, the mark is recorded as a dash.

As soon as a decision is made about the mark counter, it is cleared to prepare it for the reception of the next Morse code element. Meanwhile, the space counter is continually being incremented once every 1024 microseconds. When it exceeds twice the dot length, the program concludes that an entire Morse character has been received; and the corresponding alphanumeric character is displayed on the AIM 65 display. As the space counter is incremented further, it reaches four times the dot length; at which time the program decides that a word space has been sent, and a space appears on the AIM 65 display. At this time the space counter is cleared, the speed setting is checked to see if the operator changed the speed setting on the AIM 65, and the program returns to the wait loop to wait for the next mark.

The author is aware of receive programs that use automatic calibration of tracking on the incoming code speed. Consult the bibliography for details. My own experience is one of frustration because the presence of noise and interfering signals affects the automatic calibration, although I have heard reports that Bob Kurtz's program works nicely. In the present case, we have used manual control of the code speed with good results. Some experience and practice is useful. Bob Kurtz's program could be adapted for the AIM 65, and could also be adapted to work with the present send programs.

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TABLE I. Routine Location Table, continued.

LOCATIONS	FUNCTION
\$06EE - \$073F	Interrupt routine for Morse code receive program.
\$0750 - \$07A5	Control S routine. Converts decimal entry of speed to the number needed to load the timer.
\$07AB - \$07B5	Subroutine TMELOAD. Used to load the timer for the receive program.
\$07B6 - \$07C3	Subroutine UNTITLED. Used to display the Morse code character that has just been decoded by the receive program.
\$0820 - \$0901	Receive routine.

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Morse Code Listings

<A> = 0420	00	00	00	00
 = 0421	00	00	00	0E
<C> = 0422	00	00	00	00
<D> = 0423	0E	00	55	94
<E> = 0424	70	70	30	10
<F> = 0425	80	84	94	04
<G> = 0426	74	74	16	22
<H> = 0427	88	80	88	12
<I> = 0428	88	68	88	38
<J> = 0429	98	48	28	08
<K> = 0430	88	28	78	88
<L> = 0431	48	78	78	78
<M> = 0432	68	08	58	18
<N> = 0433	78	18	18	78
<O> = 0434	88	88	08	08
<P> = 0435	88	88	28	28
<Q> = 0436	18	28	48	54
<R> = 0437	19	41	41	40
<S> = 0438	71	05	72	07
<T> = 0439	44	48	47	4F
<U> = 0440	48	56	46	26
<V> = 0441	40	20	38	1F
<W> = 0442	42	08	41	09
<X> = 0443	51	71	20	28
<Y> = 0444	28	14	18	12
<Z> = 0445	28	28	18	12
<[> = 0446	28	28	28	28
<]> = 0447	28	28	28	11

<^> = 0488	28	30	2F	28
<_> = 0484	28	28	28	28
<`> = 0488	27	26	28	28
<~> = 0480	28	28	28	28
< > = 0408	28	28	28	28
< > = 0404	28	28	28	28
< > = 0408	28	28	28	28
< > = 0408	2F	28	28	28
<X> = 0478	28	28	28	20
< > = 0568				
✓58				
0588	78	5E1		
0581	89	LDR	#88	
0582	88	STR	8888	
0583	89	LDR	481	
0588	88	STR	8888	
0588	88	STR	8882	
058E	88	JSR	8888	
0511	R2	LDR	488	
0512	R8	LDR	488	
0515	94	STY	81, X	
0517	28	JSR	8880	
0518	28	JSR	7888	
0518	09	OMP	#7F	
051F	D8	ONE	8501	
0521	R9	LDR	#28	
0522	88	PEY		
0524	99	STR	8288, Y	
0527	88	TXR		
0528	48	PHR		

0529	28	JSR	8885	
052C	68	PLR		
052D	88	TRX		
052E	18	CLO		
052F	98	BOC	8517	
0531	09	OMP	#88	
0532	78	888	855F	
0535	09	OMP	#88	
0537	78	888	854C	
0539	78	STR	8288, Y	
053C	88	TXR		
053D	48	PHR		
053E	88	LDR	8588, Y	
0541	28	JSR	887E	
0544	28	JSR	8888	
0547	88	PLR		
0548	88	TRX		
0549	08	INX		
0549	08	ONE	8517	
054C	88	TXR		
054D	48	PHR		
054E	28	JSR	8888	
0551	28	JSR	8888	
0554	68	PLR		
0558	88	TRX		
0558	88	PEY		
0557	94	STY	84, X	
0559	08	INX		
0559	88	OPK	#88	
055D	98	BOC	8515	

Morse Code Listings, cont'd.

<K>*#055F

/50

```

055F 20 JSR 0698
0562 20 JSR 0688
0565 A9 LDA #08
0567 80 STA #484
056A A9 LDA #56
056C 80 STA #485
056F A9 LDA #08
0571 80 STA #08E
0574 A9 LDA #48
0576 80 STA #088
0579 A9 LDA #FF
057B 80 STA #08E
057E A9 LDA #FF
0580 80 STA #085
0582 A9 LDA #88
0585 80 STA 28
0587 85 STA 22
0589 A9 LDA #83
058E 85 STA 21
0590 85 STA 23
059F A8 LDY #88
0591 58 CLI
0592 A5 LDA 22
0594 05 CMP 26
0596 F8 BEQ 0592
0598 81 LDA (28),Y
059A A8 TAX
059B 28 JSR 0582
059E E6 INC 28
05A0 40 JMP 05F4
05A2 80 LDA #488,X
05A6 F8 BEQ 0506
05A8 8A BPL A
05A9 F8 BEQ 0588
05AB 48 PHA
05AC 88 BCS 0584
05AE 28 JSR 0508
05B1 40 JMP 0587
05B4 28 JSR 05E4
05B7 68 PLA
05B8 40 JMP 05A8
05BB A2 LDX #82
05BD 28 JSR 05E9
05C0 0A OEX
05C1 08 BNE 058D
05C2 68 RTS
05C4 08 CLO
05C6 EA NOP
05C8 A2 LDX #84
05CB 40 JMP 058D

```

<K>*#052E

/50

```

050B A2 LDX #81
050D 0E DEC #088
0508 28 JSR 05E9
0502 0A OEX
0504 08 BNE 0508
0506 A0 LDA #088
0509 4A LSR A
050A 88 BCS 05E2
050C EE INC #088
050F E8 INX
0510 40 JMP 0508
0512 68 RTS
0514 A2 LDX #83
0516 40 JMP 050D
0519 A3 LDA 87
051B 80 STA #497
051E 20 BIT #497
0521 18 BPL 052E
0523 68 RTS
0524 28 JSR 05A6
0527 40 JMP 0582
052A EA NOP
052B EA NOP
052C EA NOP
052D EA NOP
052E EA NOP
0530 48 PHA
0531 28 AND #E8
0532 F8 BEQ 0527
0535 68 PLA
0536 09 CMP #7F
0538 08 BNE 0512
053A 05 DEC 22
053C 08 CLO
053E EA NOP
0540 28 JSR 0685
0541 68 RTS
0542 09 CMP #58
0544 88 BCS 0511
0546 A3 LDY #88
0548 91 STA (22),Y
054A E6 INC 22
054C 28 JSR 0672
054F 28 JSR 0688
0552 68 RTS
0553 68 PLA
0554 09 CMP #13
0556 08 BNE 0528
0558 40 JMP 0758

```

<K>*#062B

/50

```

062B 08 CMP #84
062D 88 BCC 0648
062F A8 TAX
0630 86 LDX 88,Y
0632 8A TXA
0633 48 PHA
0634 80 LDA 0288,X
0637 A8 TAX
0638 58 CLI
0639 28 JSR 05A2
063C 78 BEI
063D 68 PLA
063E A8 TAX
063F 09 CMP 0683,Y
0642 F8 BEQ 0648
0644 E8 INX
0645 40 JMP 0632
0648 68 RTS
0649 09 CMP #88
064B 08 BNE 0658
064D 40 JMP 0588
0650 09 CMP #12
0652 08 BNE 0657
0654 40 JMP 0828
0657 40 JMP 08A7
065A EA NOP
065B EA NOP
065C EA NOP
065D EA NOP
065E EA NOP
065F EA NOP
0660 A2 LDX #13
0662 8A TXA
0663 48 PHA
0664 80 LDA #438,X
0667 89 ORA #88
0669 28 JSR EF7B
066C 68 PLA
066D A8 TAX
066E 0A OEX
066F 18 BPL 0662
0671 68 RTS
0672 80 STA #44C
0675 A2 LDX #83
0677 80 LDA #438,X
067A 0A OEX
067B 90 STA #438,X
067E E8 INX
067F E8 INX
0680 E8 ORX #15

```

Morse Code Listings, cont'd.

<K>*=-0682

/50

```

0682 90 BCC 0677
0684 60 RTS
0685 A2 LDX #10
0687 B0 LDA A43A, X
0688 E8 INX
068B 90 STA A43A, X
068E 0A DEX
068F 0A DEX
0690 10 BPL 0697
0692 A9 LDA #20
0694 80 STA A43A
0697 20 JSR 0660
0698 60 RTS
069B A2 LDX #13
069D A9 LDA #20
069F 90 STA A43B, X
06A2 0A DEX
06A3 10 BPL 069F
06A5 60 RTS
06A6 30 SEC
06A7 A5 LDA 22
06A9 E5 SBC 20
06AB 09 CMP #12
06AD B0 BCS 06BF
06AF 85 STA 24
06B1 30 SEC
06B2 A9 LDA #11
06B4 E5 SBC 24
06B6 AA TAX
06B7 A9 LDA #20
06B9 90 STA A43A, X
06BC 20 JSR 0660
06BF 60 RTS
06C0 40 PHR
06C1 8A TXR
06C2 40 PHR
06C3 98 TYR
06C4 40 PHR
06C5 A0 LDA A000
06C8 29 AND #10
06CA F0 BEQ 06E6
06CC A0 LDA A000
06CF 30 BNE 06D7
06D1 20 JSR 050B
06D4 40 JMP 06DA
06D7 20 JSR 05E4
06DA A0 LDA A000
06DD 0A ASL .A
06DE 10 BPL 06DC
06E0 60 PLA

```

<K>*=-06E1

/50

```

06E1 A8 TAY
06E2 68 PLA
06E3 AA TAX
06E4 68 PLA
06E5 40 RTI
06E6 20 BIT A000
06E9 50 SVC 06EE
06EB 40 JMP 0904
06EE 20 JSR 07AB
06F1 A9 LDA #20
06F3 20 BIT A000
06F6 F0 SED 06F3
06F8 A0 LDA A001
06FB 10 BPL 0710
06FD E5 INC 1A
06FF D0 BNE 0703
0701 E5 INC 1B
0703 EA NOP
0704 EA NOP
0705 EA NOP
0706 EA NOP
0707 EA NOP
0708 EA NOP
0709 60 PLA
070A A0 TAY
070B 60 PLA
070C AA TAX
070D 60 PLA
070E 40 RTI
070F EA NOP
0710 20 JSR 078B
0713 E5 INC 18
0715 D0 BNE 0719
0717 E5 INC 19
0719 A5 LDA 18
071B 05 CMP 10
071D 90 BCC 06F1
071F D0 BNE 0727
0721 A5 LDA 18
0723 05 CMP 10
0725 90 BCC 06F1
0727 A9 LDA #00
0729 85 STA 1A
072B 85 STA 1B
072D A9 LDA #20
072F 20 BIT A000
0732 F0 BEQ 072F
0734 A0 LDA A001
0737 10 BPL 0710
0739 A0 LDA A001

```

<K>*=-0730

/50

```

0730 50 CLI
073D 40 JMP 085E
0740 EA NOP
0741 EA NOP
0742 EA NOP
0743 EA NOP
0744 EA NOP
0745 EA NOP
0746 EA NOP
0747 EA NOP
0748 EA NOP
0749 EA NOP
074A EA NOP
074B EA NOP
074C EA NOP
074D EA NOP
074E EA NOP
074F EA NOP
0750 20 JSR E93C
0753 40 PHR
0754 80 STA A43B
0757 20 JSR 0660
075A 60 PLA
075B 30 SEC
075C E9 SBC #E0
075E 0A ASL .A
075F 0A ASL .A
0760 0A ASL .A
0761 0A ASL .A
0762 85 STA 11
0764 20 JSR E93C
0767 40 PHR
0768 80 STA A439
076B 20 JSR 0660
076E 60 PLA
076F 30 SEC
0770 E9 SBC #D0
0772 10 CLC
0773 65 ADC 11
0775 40 PHR
0776 29 AND #F0
0778 4A LSR A
0779 85 STA 10
077B 4A LSR A
077C 4A LSR A
077D 10 CLC
077E 65 ADC 10
0780 85 STA 10
0782 60 PLA
0783 29 AND #0F

```

Morse Code Listings, cont'd.

<K>#=#8785

/35

```

8785 65 ADC 10
8787 85 STA 10
8789 38 SEC
878A 82 LDX #00
878C 89 LDA #94
878E 85 STA 00
8790 89 LDA #04
8792 85 STA 00
8794 85 LDA 00
8796 85 SBC 10
8798 85 STA 00
879A 10 LDA 00
879C 89 SBC #00
879E 85 STA 00
87A0 88 INX
87A1 88 BCS 8794
87A3 86 STX 07
87A5 88 RTS
87A6 8A NOP
87A7 8A NOP
87A8 8A NOP
87A9 8A NOP
87AA 8A NOP
87AB 89 LDA #00
87AD 88 STA #000
87B0 89 LDA #04
87B2 88 STA #000
87B5 88 RTS
87B6 98 TYR
87B7 89 ORR #00
87B9 88 TRY
87BA 89 LDA #400,Y
87BC 88 JSR #072
87BE 88 JSR #000
87C3 68 RTS

```

```

8839 86 ASL 14
883B 26 ROL 15
883D 86 ASL 15
883F 26 ROL 17
8841 86 ASL 16
8843 26 ROL 17
8845 88 ASL A
8846 46 LSR 10
8848 86 ROR 10
8849 89 LDA #00
884C 85 STB 1A
884E 85 STB 10
8850 89 LDA #02
8852 88 STA #00E
8855 88 LDY #01
8857 8D LDB #001
885A 58 CLI
885B 4C JMP #050
885E 88 JSR #75B
8861 86 INC 1A
8863 88 BNE #007
8865 86 INC 10
8867 85 LDA 10
8869 85 CMP 10
886B 88 BCC #07F
886D 88 BNE #075
886F 85 LDA 1A
8871 85 CMP 10
8873 88 BCS #07F
8875 89 LDA #20
8877 88 AND #00D
887A 88 BEQ #075
887C 4C JMP #05E
887E 85 LDA 10
8881 85 CMP 10
8883 88 BCC #00E
8885 88 BNE #000

```

```

889E 88 INY
889F 88 BCC #00E
88A1 88 LDA 10
88A3 85 CMP 12
88A5 98 BCC #07E
88A7 89 LDA #00
88A9 82 LDX #00
88AB 88 JSR #770
88AD 89 LDA #00
88B0 85 STA 10
88B2 85 STA 12
88B4 89 LDA #20
88B6 88 AND #00D
88B9 88 BEQ #004
88BB 88 JSR #750
88BD 86 INC 1A
88C0 86 BNE #004
88C2 86 INC 10
88C4 85 LDA 10
88C6 85 CMP 15
88C8 98 BCC #004
88CA 88 BNE #002
88CC 85 LDA 10
88CE 85 CMP 14
88D0 98 BCC #004
88D2 88 JSR #706
88D5 88 LDY #01
88D7 89 LDA #20
88D9 88 AND #00D
88DB 88 BEQ #007
88DE 88 JSR #700
88E1 86 INC 1A
88E3 88 BNE #007
88E5 86 INC 10
88E7 85 LDA 10
88E9 85 CMP 17
88EB 98 BCC #007

```

<K>#=#8820

/50

```

8820 78 SEI
8821 85 LDA 07
8823 85 STA 12
8825 85 STA 14
8827 85 STA 16
8829 85 STA 10
882B 89 LDA #00
882D 85 STA 13
882F 85 STA 15
8831 85 STA 17
8833 85 STA 10
8835 85 STA 19
8837 85 STA 10

```

<K>#=#8867

/50

```

8867 85 LDA 10
8869 85 CMP 10
886B 98 BCC #00E
886D 88 TYR
886E 8A ASL A
886F 88 TRY
8890 85 LDA 10
8892 85 CMP 15
8894 98 BCC #0A1
8896 88 BNE #00E
8898 85 LDA 10
889A 85 CMP 14
889C 98 BCC #0A1

```

<K>#=#88ED

/50

```

88ED 88 BNE #07E
88EF 85 LDA 1A
88F1 85 CMP 16
88F3 98 BCC #007
88F5 88 JSR #706
88F8 4C JMP #020
88FB 89 LDA #00
88FD 82 LDX #01
88FF 4C JMP #00E
8902 8A NOP
8903 8A NOP
8904 8D LDA #004
8907 8D LDA #40E

```

Morse Code Listings, conclusion

```

0908 C9 CMP #FF
090C F0 BEQ 0915
090E 00 ORA A47F
0911 49 EOR #FF
0913 D0 BNE 0950
0915 A2 LDX #00
0917 8E STX A42A
091A 20 JSR ED05
091D 88 DEY
091E 30 BMI 095C
0920 A9 LDA #0F
0922 80 STA A490
0925 A0 LDA A482
0928 4A LSR A
0929 B0 BCS 094B
092B A2 LDX #03
092D A9 LDA #7F
092F 38 SEC
0930 6A ROR A
0931 48 PHA
0932 20 JSR ED08
0935 A0 LDA A492
0938 4A LSR A
0939 90 BCC 0941
093B 68 PLA
093C 08 DEY
093D D0 BNE 092F
093F F0 BEQ 093C
0941 68 PLA
0942 A0 LDA A42B
0945 49 EOR #FF
0947 AA TAX
0949 EE INC A42A
094B 20 JSR ED05
094E 88 DEY
094F D0 BNE 095A
0951 A0 LDA A42B

```

<K>#0954
/50

```

0954 C9 CMP #F7
0956 80 BCS 095C
0958 90 BCC 099C
095A 30 BMI 099C
095C EA NOP
095D EA NOP
095E EA NOP
095F 98 TYR
0960 0A ASL A
0961 0A ASL A
0962 0A ASL A
0963 A8 TRY
0964 A0 LDA A42B
0967 4A LSR A

```

```

0968 90 BCC 096D
096A D8 INY
096B D0 BNE 0967
096D B9 LDA F421,Y
0970 48 PHA
0971 8A TXA
0972 F0 BEQ 0958
0974 29 AND #10
0976 F0 BEQ 097E
0978 68 PLA
0979 29 AND #3F
097B 4C JMP 0999
097E 68 PLA
097F 48 PHA
0980 29 AND #40
0982 D0 BNE 0998
0984 68 PLA
0985 48 PHA
0986 29 AND #0F
0988 F0 BEQ 0998
098A C9 CMP #0C
098C B0 BCS 0992
098E 68 PLA
098F 29 AND #EF
0991 D0 BNE 0999
0992 68 PLA
0994 09 ORA #10
0996 D0 BNE 0999
0998 68 PLA
0999 20 JSR 0600
099C 68 PLA
099D A9 TRY
099E 68 PLA
099F AA TAX
09A0 A9 LDA #00
09A2 80 STA A42A

```

<K>#09A5
/50

```

09A5 68 PLA
09A6 40 RTI
09A7 C9 CMP #0C
09A9 D0 BNE 09AF
09AB 58 CLI
09AC 4C JMP 09AC
09AF C9 CMP #0B
09B1 D0 BNE 09BD
09B2 A9 LDA #02
09B5 80 STA A00E
09B8 4C JMP 058F
09BB C9 CMP #10
09BD D0 BNE 09C7
09BF A0 LDA A411
09C2 49 EOR #00
09C4 80 STA A411
09C7 68 RTS

```

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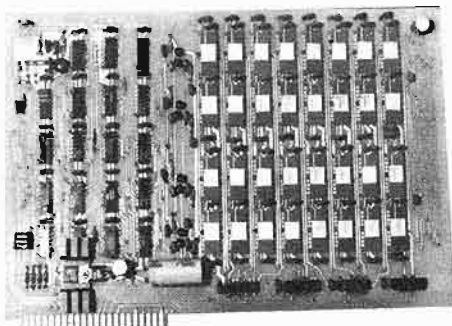
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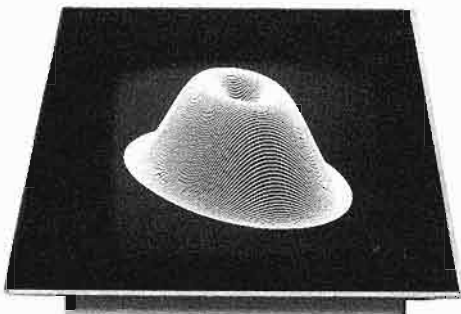


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MICRO is interested in having a monthly feature on 6502-related clubs. We would like to publish the names, locations and activities of groups that could be of interest to our readers. We attempted to start this feature in the past; but because of technical and publication problems, it fell by the wayside. We are now ready to get it underway.

If you are a member of such a club, have your representative register your group with us. A form for this purpose is included on our tear-out sheet. In return, we will send a free one-year subscription to MICRO for your club's library.

We would like this feature to be as helpful to our readers as possible. We welcome any information that will be of interest to other clubs; i.e., what clubs do, how they get started, what they publish, meeting format, their purpose, etc.

We are publishing as complete a list as we presently have of interested clubs. We will update it periodically, much like our bibliography section. Start increasing your membership and give your group new exposure by telling others about yourselves.

Apple Group - New Jersey

Meets the 4th Friday of every month, 7:00 p.m., at:
Union County Technical Institute
1776 Raritan Road
Scotch Plains, N.J.
Contact: Apple Group-N.J.
c/o Steve Toth
1411 Greenwood Drive
Piscataway, N.J. 08854
Tel: (201) 968-7498

The NYC User Group

The Drysdale Security
55 Water Street
New York, NY 10004
Contact: Pres. Neil Shapiro
home: (516)-579-4295 (after 6 p.m.) or
office: (212)269-4808

PACS PET User Group

Meets the third Saturday (11:00 a.m.) every month in the:
Science Building
LaSalle College
20th and Onley Avenue
Philadelphia, PA 19191

Washington Apple Pi

Meets the fourth Saturday (9:30 a.m.) every month at:
George Washington University
Rm. 206, Tompkins Hall
23rd and H Streets N.W.
Washington, DC
Write: Washington Apple Pi
P.O. Box 34511
Washington, DC 20034
or call: Sandy Greenfarb, (301)674-5982
Publishes a monthly newsletter.

South Carolina Apple

Meets second Tuesdays (7:30 p.m.) at:
Byte Shop
1920 Blossom Street
Columbia, SC 29205
Write: P.O. Box 70278
Charleston Heights, SC 29405

WAKE —

Washington Area Kim Enthusiasts
Meets the third Wednesday (7:30 p.m.) of every month at:
McGraw-Hill Continuing Education
Center in Washington, D.C.
Contact: WAKE, c/o Ted Beach
5112 Williamsburg Boulevard
Arlington, VA 22207
or phone (703)538-2303

Miami Apple Users Group (M.A.U.G.)

Contact: David Hall, Secretary
2300 N.W. 135th Street
Miami, FL 33167

Sun Coast Apple Tree (SCAT)

Meets first and third Thursdays (7:00 p.m.) at:
The Computer Store
21 Clearwater Mall
Clearwater, FL 33516

COACH —

Central Ohio Apple Computer Hobbyists
Meets the third Saturday (1:00 to 5:00 p.m.) of every month
Contact: Tom Mirmitch
1547 Cunard Road
Columbus, Ohio 43227
Phone (614) 237-3380

APPLE Dayton

Meets the second Wednesday of odd numbered months and the second Thursday of even numbered months (7:30 p.m.) at:
Computer Solutions
Contact: Robert W. Rennard
2281 Cobble Stone Court
Dayton, OH 45431
Phone: (513) 426-3579

Madison Pet Users Club

Meets monthly at:
Washington Square Building
1400 East Washington Avenue
Madison, WI 53913
Contact: Ben A. Stewart
501 Willow
West Baraboo, WI 53913

Micro and Personal Computer Club of St. Louis

Meets monthly at:
Futureworld, Inc.
12304 Manchester Road
St. Louis, MO 63131
Contact: Mr. Kunihiro Tanaka
314) 645-4431

Tulsa Computer Society

Meets the last Tuesday (7:30 p.m.) of every month, at:
Tulsa Vo-Tech School, seminar center,
3420 S. Memorial Drive
Tulsa, OK

The Apple Corps

Meets the second Saturday (2-5 p.m.) of each month at:
Greenhill School
14255 Midway Road
Dallas, TX

Apple User Group

Meets the second Tuesday of each month at:
High Technology of Tulsa Computer Store
2601D S. Memorial Drive, Tulsa

For information on both of the above groups, write:

The Tulsa Computer Society
P.O. Box 1133
Tulsa, OK 74101

Appleseed

Meets monthly at:
The Computer Shop
6812 San Pedro
San Antonio, TX 78216
(512) 828-0553

The Austin Apple Corps

Meets first and third Tuesdays (7:00 p.m.) of every month.

Contact: Mike Palmore, (512) 442-4871/447-0332; Kris Cobb (512)837-7228/443-7711; or Lenny Fein (512)441-3220/471-1154.

The L.A. Apple Users Group

Meets the first Friday (7:30 p.m.) of every month at:

Allstate Savings Community Room
8800 S. Sepulveda Boulevard
Los Angeles, CA.

Contact: Philip A. Wasson
9513 Hindry Place
Los Angeles, CA 90045

The San Fernando Valley 6502 Users Club

Meets the second Tuesday (8:00 p.m.) of every month at:
Computer Components Inc. of Burbank
3808 West Verdugo Avenue
Burbank, CA 91505

Contact: Larry Goga
3816 Albright Avenue
Los Angeles, CA 90066
Publishes a monthly newsletter.

Honolulu Apple Users Society

Meets the first Monday of each month at:
Computerland Store in Honolulu.

Contact: Bill Mark
98-1451-A Kaahumanu Street
Aiea, Hawaii 96701
Phone: (808)488-2026

Northwest Pet User's Group

Contact: John F. Jones
2134 NE 45th Avenue
Portland, OR 97213
Phone: (503)281-4908

Northwest Pet Users Group

Meets monthly at:
Seawel Marketing
315 B N.W. 85
Seattle, WA
Contact: Jeffrey Dukes
15346 SE 307
Kent, WA 98031
Phone: (206)631-1973

A.P.P.L.E.

Meets the third Tuesday of every month.
Contact: Ralph Thiers
8710 Salty Drive, NW
Olympia, WA 98502

Apples British Columbia Computer Society

Meets the first Wednesday of every month.

Contact: Gary B. Little
101-2044 West Third Avenue
Vancouver, British Columbia,
Canada V6J 1L5
Phone: (604)731-7886

The North London Hobby Computer Club

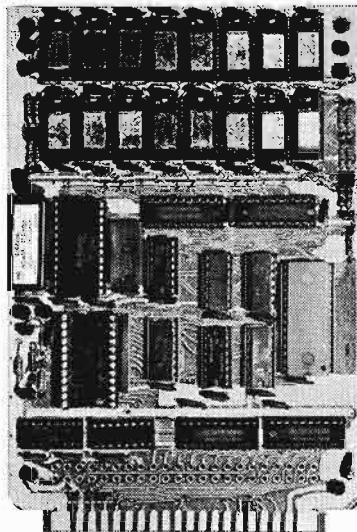
Different groups within the club meet many times weekly, in addition to a club-wide monthly meeting.

Contact: Stephanie Bromley
The Polytechnic of North London
Holloway, London N7 8DB
Phone: 01-607-2789

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Jack Robert Swindell
P.O. Box 8193
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The OSI Superboard 11, Challenger 1P is a great machine — fast so you can really get the job done. Not bad considering that it is running at under 1MHZ. Wouldn't it be even nicer running at 2? Don't start jumping up and down and barking yet, we have a few hurdles to jump first. They are not really tall ones, but you had better know where they are at instead of stumbling into them.

The model 600 board was designed to run the 6502 at about 983KHZ or almost 1MHZ. This meant that they could keep the cost down by having highly efficient software resident in ROM's (firmware) do the magic of making process time short instead of sloppy software with a faster clock rate to help make up for it. The cost saving is in the RAM...it only seems to be good for 1MHZ or thereabouts. Apparently the same Basic in ROM is used in several OSI computers with the I/O handling controlled by a monitor/support ROM unique to each model (or series). If this really is the case (does anyone know for sure?) then the Basic in ROM must be able to operate at 2MHZ to prevent having to stock multiple grades of ROM (which is a rather expensive proposition) for the different speeds of CPU's.

The other thing that makes me think that there is only one grade of Basic in ROM is that there are no suffix marks on the ROM's to indicate that they might have been sorted for speed. It is possible that the monitor/support ROM was only specified to guarantee operation at 1MHZ as that is the intended processor operating speed for the 600 series board.

As this ROM is probably unique to the model 600 and would not appear on the 2MHZ board, the 2MHZ capability may not have been specified for this chip.

There is one other thing to consider before delving into the hardware aspect of this project. Do you have any optional boards tied into your 600 board? Especially memory...the original factory-installed RAM on my card was not able to make 2MHZ; therefore, I most certainly wouldn't count on their expansion RAM handling double the normal recommended speed. Translated: The memory that you already have probably won't work at 2MHZ and will have to be replaced (OUCH). Perhaps you could trade with someone. Well, let's not jump the gun and start ordering parts yet, there is always that chance that *your* memory might be different than mine and will work OK...I hope so. My originals were 2114L's by SEMMI. I don't know what happens if you have a mini-floppy tied in and then double the speed. Also assume that your warranty is shot once you modify it. You might want to wait until it expires.

The first thing to do is to decide whether or not you want to go any further than just reading this article. *Remember:* Neither the author nor MICRO guarantee the safety or operation of this modification, nor should you expect the manufacturer or service department to honor any warranties after you have modified your equipment. Mostly what I am saying is that if you don't understand what you are doing: *DON'T DO IT!* And...if you goof up and ruin your machine *you did it yourself.* I don't know how to say it in proper legal-

ese, but you get the picture.

TURN OFF THE POWER FIRST!!!!

The illustration applies to my model 600 CPU, revision B. What this modification is doing is moving the tap on the clock circuit divider chain one divide by two closer to the oscillator. You're sure that you want to do this? OK...cut the line as shown in the illustration. You have just severed the clock line going to pin 37 on the 6502. Take a small piece of insulated wire and make a jumper like in the illustration. You won't have to strip off very much insulation at each end to do the job. Solder it in, again see the illustration, taking care not to short any of those teeny conductors nearby. Now the CPU will have twice the clock speed as before. Now to see how it turned out.

I hope your memory makes it as is...we'll soon see. Connect the video monitor cable and turn on the monitor. Do not connect any off-card peripherals of any sort yet. Now apply power to the CPU and press BREAK. Does the screen show any characters other than D/C/W/M? If so, jump to the next paragraph. Press C and finish off the usual initialization routine. If there are any incorrect characters, jump to the next paragraph. Try to run a few simple two or three line programs and solve some easy problems in the command mode. If anything didn't work satisfactorily, jump to the next paragraph. Congratulations, you are now the owner of a super-Superboard. Keep an eye open just in case any problems might develop until you feel sure that all is OK. Branch to the next sub-heading.

If you are reading this paragraph then you have a minor problem to solve. Most probably your RAM is a bit too slow. Try to borrow four 2114 RAM's known to be good at 2 or more MHZ. Pull out all ten (or eighteen) RAM's on your CPU card (note polarity), both program and video memory. Look in the back of your User's manual for the locations of U31, U39, U40 and U45. Plug in the faster 2114's here making sure that you get them in the same way that the others came out. Try to run through the initialization tests of the previous paragraphs. It should say that it has 255 bytes free. If this doesn't work, you can either try one more set of different RAM's in the hope that one of them still wasn't fast enough. No go? I'm sorry...probably one of the ROM's is a bit slow. Well, just reverse the order of steps in the modification, restore the original memory chips (making sure to put a jumper in where you cut the line and removing your modification jumper) and you're none the worse for wear.

COMMAND MODE STRING PRINTING

I have one small item of curiosity to throw in before I vector off into oblivion. Type (in command mode) "?'67 or 68 characters", press RETURN. It may or may not print the string and will almost always print a syntax error at some non-existent line number. Branch to next article.

HAPPY COMPUTING!

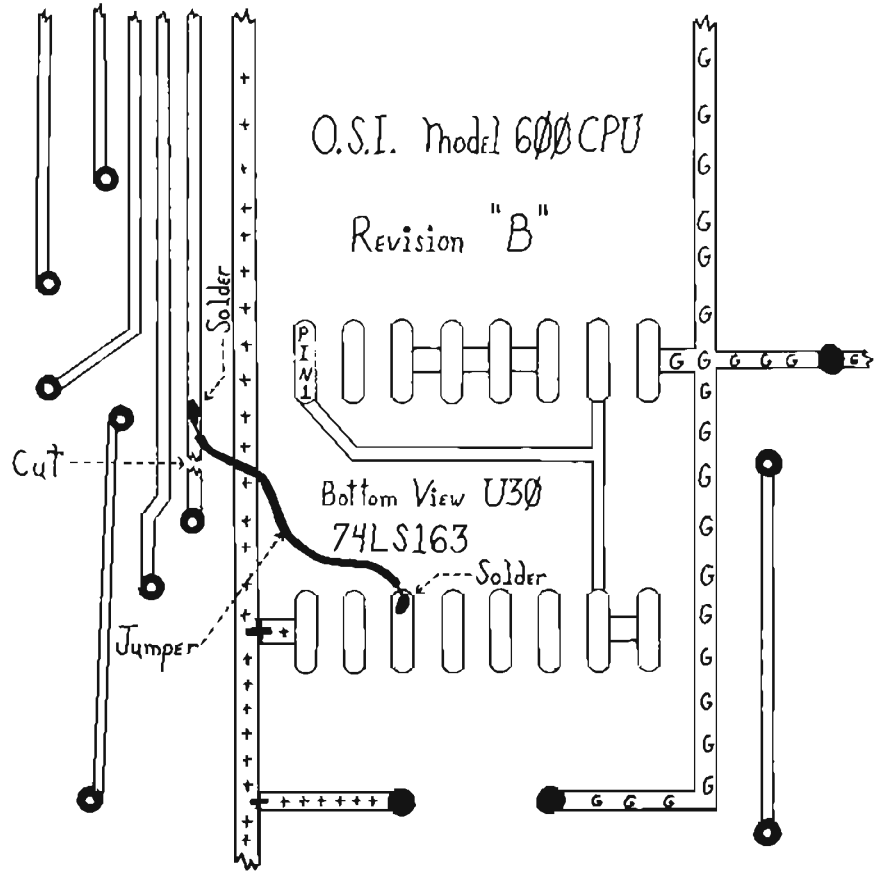


Figure 1

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By Roger Wagner

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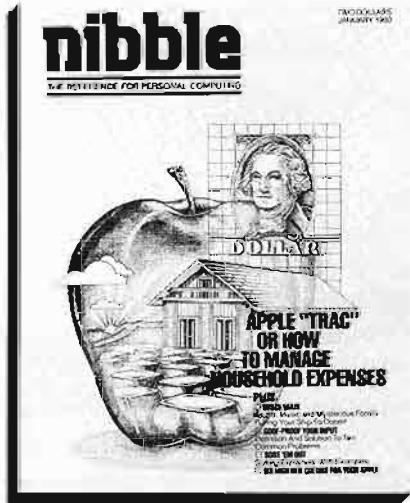
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KIM-1 Tape Recorder Controller

Some techniques for using a 6502 micro for controlling switches are presented. The particular application is for a KIM to control a tape deck, but the concepts are quite broad in scope.

Michael Urban
General Electric
SPD Box 43
Auburn, NY 13021

OBJECTIVE

The Kim-1 microcomputer is to be used to control the four functions (play, rewind, wind and stop) of a Tandbert 9000X open-reel tape deck by way of the remote control socket at the back of the deck. This control will enable the user to program the computer to automatically locate and play a sequence of songs previously selected.

METHOD

The heart of the operating program is the tape counter displayed on the address LED's which simulate the mechanical tape counter on the deck itself. The actual program increments or decrements this counter, compares the desired location to the present counter, and then directs the tape deck on the result of that comparison. A description of each of the blocks of the program flow chart follows:

Initialization-

Here the counter, data register, and x and y registers are cleared. The data direction register is set to FF for an output condition, the x-register is loaded with the first song selection at location 0000 plus the y-register. The contents of both registers are then saved, using a STORE subroutine.

Compare-

The high order byte of the counter (OOFB) is compared with the contents of location 0050 plus the x-register. This location is reserved for the high order bytes of any song starting location. If the result is either positive or negative, the program branches to wind or rewind respectively. If the result is zero, the low order byte must be compared. Because of differing branch instructions, there are separate wind compares and rewind compares. Each of these takes the low order bytes of the counter (OOFB) and compares it to the contents of location 0060 plus the x-register. The program then goes to either wind, rewind or play, depending on the results.

Wind-

A 08 is placed in the data register to put the tape deck in the wind mode. The tape counter is incremented by adding 01 to 00FA. A delay loop is set up with the interval timer and the counter displayed using the SCANDS subroutine. Jump to cmp.

Rewind-

A 01 is placed in the data register to put the tape deck in the rewind

mode. The tape counter is decremented by subtracting 01 from 00FA. A delay loop is again set up with the interval timer and the counter displayed using the SCANDS subroutine. Jump back to Compare.

Stop/Wait-

A 04 is stored in the data register to stop the tape deck. Another delay loop is utilized to wait for the deck to come to a halt before putting it in the play mode. The counter is displayed on the LED's.

Play-

The contents of the x-register are placed in 00F9 so that the next display will show the song selection while playing it. A 02 is placed in the data register to put the tape deck in the play mode. The counter is incremented by adding 01 to 00FA. A delay loop is set up using the interval timer. The high order byte of the counter is now compared to the contents of location 0070 plus the x-register. This is the location of the ending location of the selected song, high order byte. If the high order bytes are not equal, the program branches back to Play. If the high order bytes are equal, the low order

bytes must be compared. The contents of the low order byte of the counter (OOFA) are now compared to the contents of the address 0080 plus the x-register which is the address of the ending location, low order byte, of the selected song. If the low order byte comparison results in a zero, the end of a song has been reached. The program sits in a delay loop waiting for the deck to catch up. The y-register is then incremented so that the next song selection can be made. Jump back to Begin.

The Interface-

Through experimentation with the remote control socket, it was found that a short between any of the function pins and ground would cause the deck to operate in that mode. A current of 2mA was measured with a short circuit to ground. Later, it was found that a resistor to ground also worked. With 2K between the function pin and ground, a lower current of 1mA was obtained. This was ideal for our purposes. Relays were considered as the interface element

but rejected because of cost and layout considerations.

The 4016 CMOS analog/digital switch was decided upon. It is an integrated circuit containing four independent switches of the configuration in figure 3. An overall view of the basic interface is pictured in figure 1. The actual wiring diagram is seen in figure 2. A 5-volt signal coming from any of the outputs PA0-PA3 will cause a switch closure in the following order:

- PA0-Rewind (01)
- PA1-Play (02)
- PA2-Stop (04)
- PA3-Wind (08)

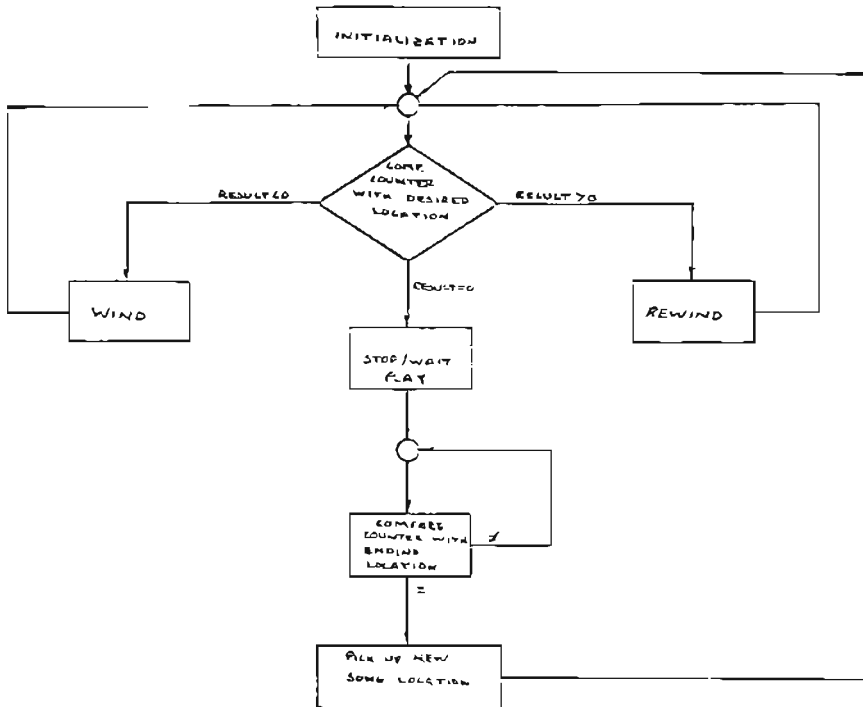
The numbers in parenthesis indicate the number that must be in the data register for that particular function to be performed. The resistors in figure 2 are for current limiting through the switch.

SUMMARY

For the most part, the project was a success. The only problem encountered was that of trying to synchronize the simulated tape counter speeds to those of the mechanical one on the tape deck. To better explain this, figure 4 is helpful. As can be seen in figure 4a, the KIM's tape counter is a very linear device unlike that of the deck's very non-linear counter in figure 4b. In the wind or rewind modes, the two could never be matched because of this non-linearity. Therefore, it was decided upon to only demonstrate the program's ability to control the tape deck and locate selections on the computer tape counter. This the program did well.

The ultimate way to circumvent this problem would be to actually couple the computer to the tape deck through an optical or magnetic pick-up on one of the tape reels. In this way, the KIM would always know precisely where the tape was located. If, for some reason, this was not possible, a linear approximation could be programmed into the computer to simulate the acceleration curve of the mechanical tape counter. This would consist of three or four loops of differing speeds cascaded together to form a curve like that of figure 4c.

In recent years, commercial manufacturers have been incorporating a similar program-locating feature into cassette decks. The most notable is the Sharp RT-3388A which has its own dedicated microprocessor which will locate a particular section of the tape requested and plays from there on; it does not have the ability of playing any sequence of songs asked for by the user. In this respect, our program is superior.



REEL-TO-REEL INTERFACE

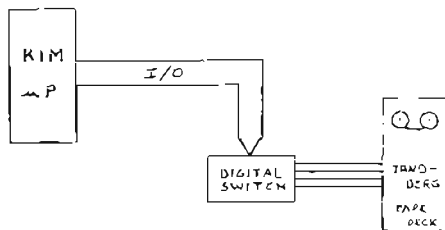
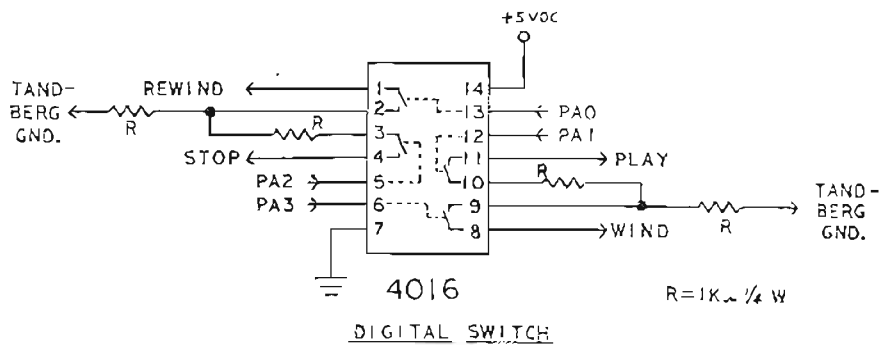
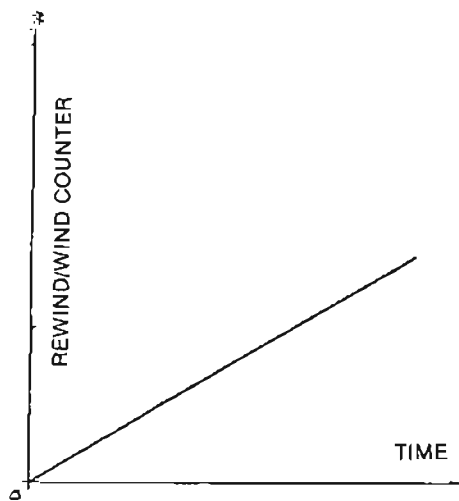


FIGURE 1



DIGITAL SWITCH

FIGURE 2



A: KIM-1 Tape Counter

INTERNAL SCHEMATIC

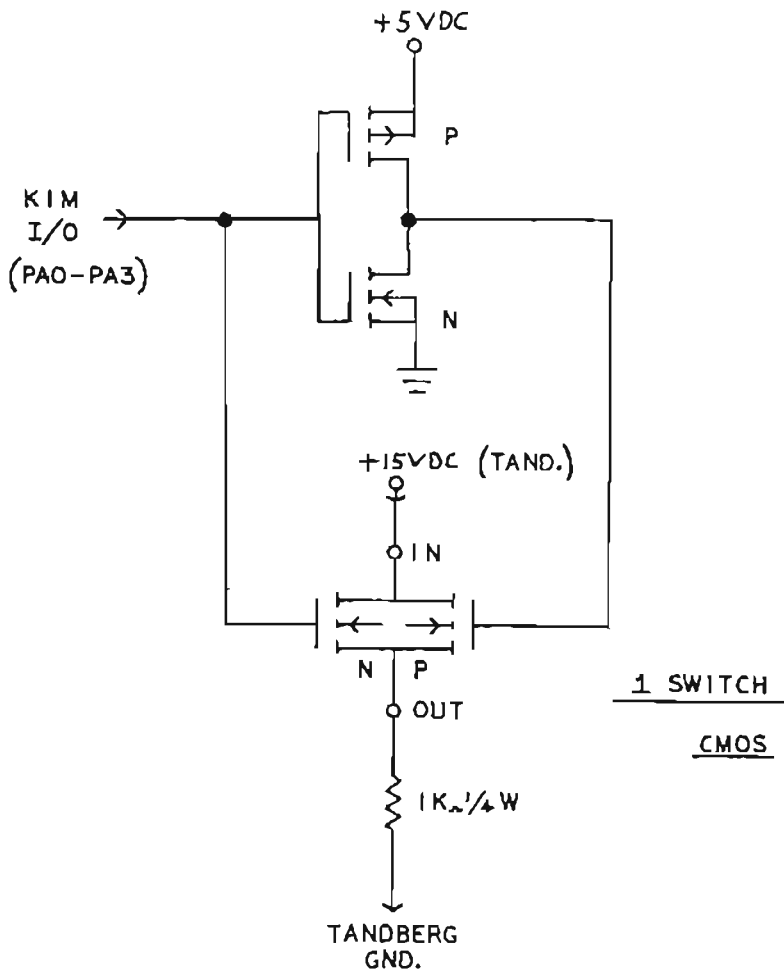
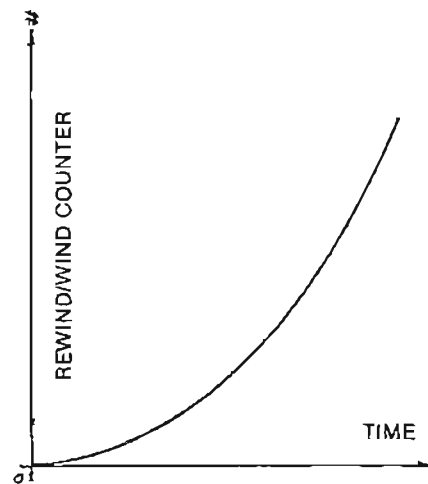
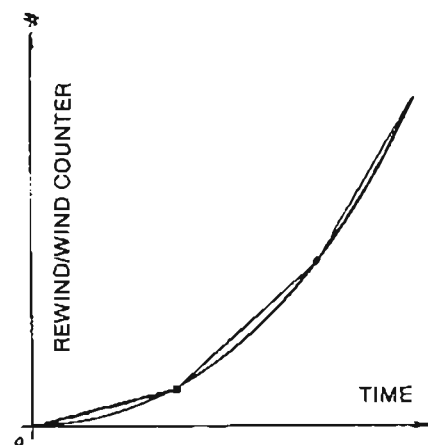


FIGURE 3



B: Tape disk counter



C: Linear approximation

Figure 4

bytes must be compared. The contents of the low order byte of the counter (OOFA) are now compared to the contents of the address 0080 plus the x-register which is the address of the ending location, low order byte, of the selected song. If the low order byte comparison results in a zero, the end of a song has been reached. The program sits in a delay loop waiting for the deck to catch up. The y-register is then incremented so that the next song selection can be made. Jump back to Begin.

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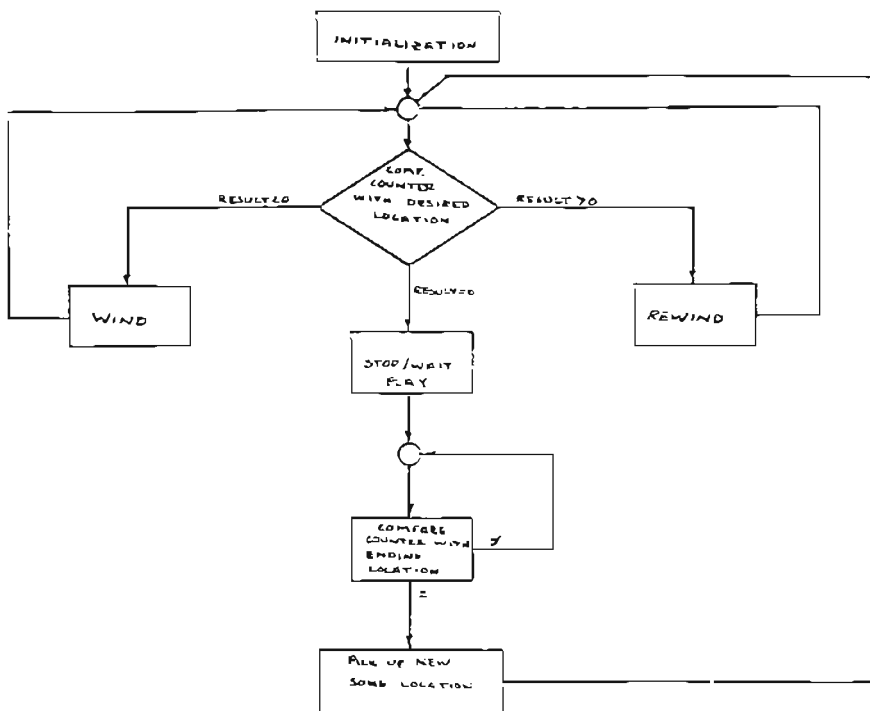
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In recent years, commercial manufacturers have been incorporating a similar program-locating feature into cassette decks. The most notable is the Sharp RT-3388A which has its own dedicated microprocessor which will locate a particular section of the tape requested and plays from there on; it does not have the ability of playing any sequence of songs asked for by the user. In this respect, our program is superior.



REEL-TO-REEL INTERFACE

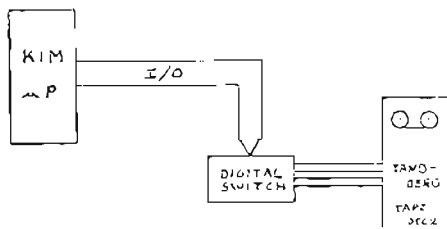


FIGURE 1

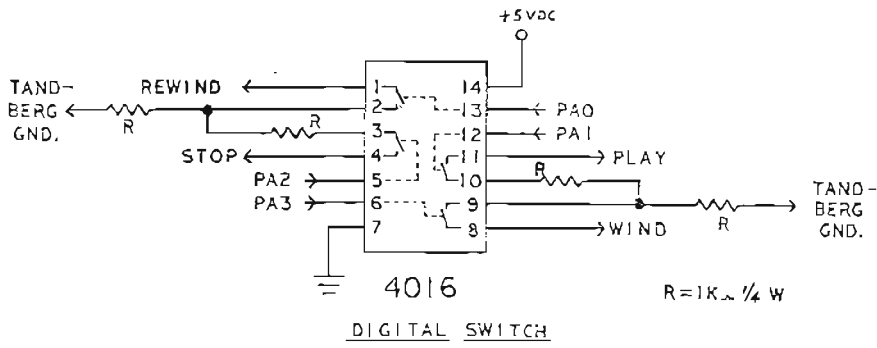
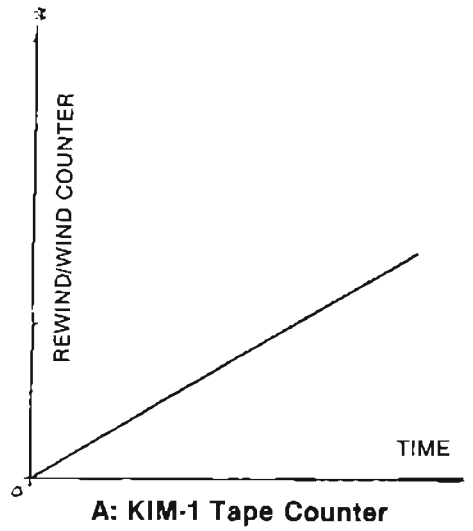


FIGURE 2



INTERNAL SCHEMATIC

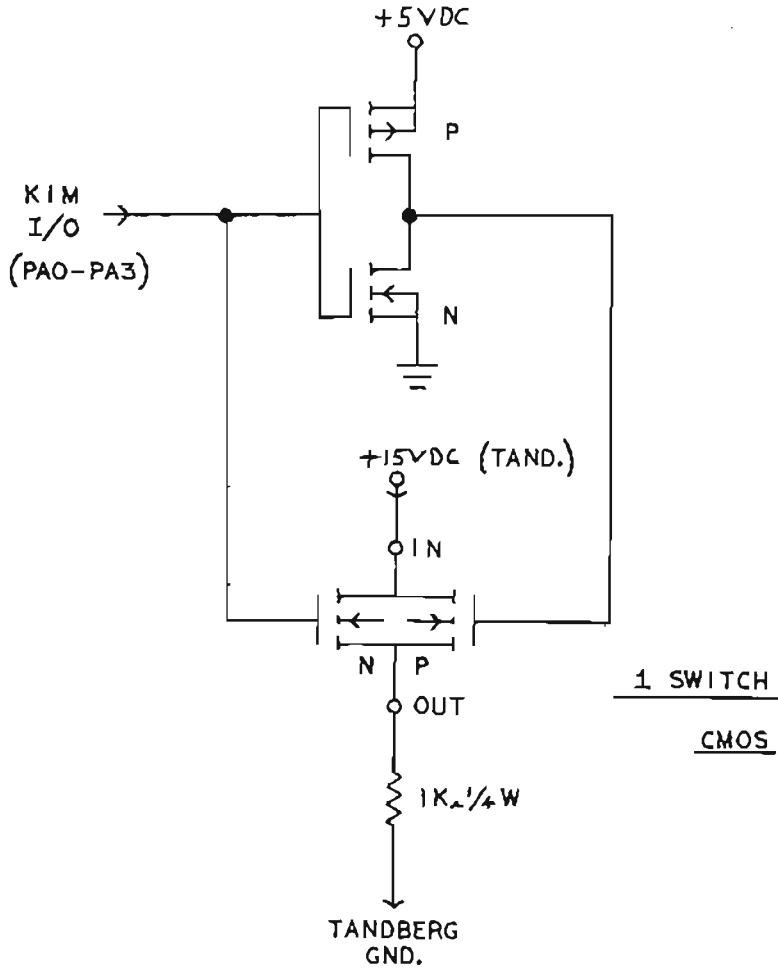
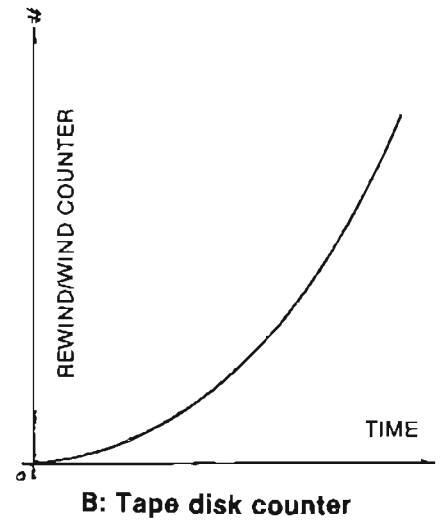
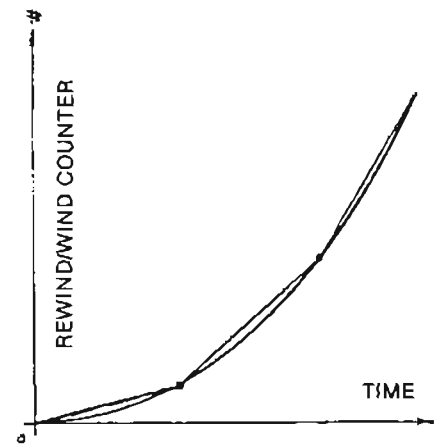


FIGURE 3



B: Tape disk counter



C: Linear approximation

Figure 4

Address	Instruction	Label	Op Code	Operand	Address	Instruction	Label	Op Code	Operand
0210	F8		SED		02BB	A9 5F	LOOP2	LDA	#\$5F
0211	18		CLC		02BD	8D 07 17		STA	+1024I.T.
0212	A9 00		LDA	#\$00	02C0	20 1F 1F	DISPLAY	JSR	SCANDS
0214	85 20		STA	0020	02C3	2C 07 17		BIT	I.T.
0216	85 21		STA	0021	02C6	10 F8		BPL	DISPLAY
0218	85 23		STA	0023	02C8	C6 24		DEC	0024
021A	85 F9		STA	00F9	02CA	D0 EF		BNE	LOOP2
021C	85 FA		STA	00FA	02CC	38		SEC	
021E	85 F8		STA	00FB	02CD	4C 38 02		JMP	*PULL
0220	8D 00 17		STA	PAD	02D2	8A	STOP	TXA	
0223	A9 01		LDA	#\$01	02D3	85 F9		STA	F9
0225	85 22		STA	0022	02D5	A9 04		LDA	#\$04
0227	A9 FF		LDA	#\$FF	02D7	8D 00 17		STA	PAD
0229	8D 01 17		STA	PADD.	02DA	A9 0A		LDA	#\$0A
022C	A2 00		LDX	#\$00	02DC	85 26		STA	0026
022E	A0 00		LDY	#\$00	02DE	A9 FF	LOOP3	LDA	#\$FF
0230	B6 00	BEGIN	IDX,Y	0000	02E0	8D 07 17		STA	+1024I.T.
0232	20 20 03		JSR	STORE	02E3	20 1F 1F	DISPLAY	JSR	SCANDS
0235	4C 3E 02		JMP	COMPHI	02E6	2C 07 17		BIT	I.T.
023B	20 54 03	*PULL	JSR	PULL	02E9	10 F8		BPL	DISPLAY
023C	20 70 03		JSR	STORE	02EB	C6 26		DEC	0026
023E	A5 FB	COMPHI	LDA	FB	02ED	D0 EF		BNE	LOOP3
0240	D5 50		CMP,X	0050	02EF	A9 02		LDA	#\$02
0242	F0 05		BEQ	COMPL0	02F1	8D 00 17		STA	PAD
0244	10 10		BPL	REWIND	02F4	18		CLC	
0246	4C 74 02		JMP	WIND	02F5	A5 22	PLAY	LDA	0022
0249	A9 01	COMPL0	LDA	#\$01	02F7	65 FA		ADC	FA
024E	2D 00 17		AND	PAD	02F9	85 FA		STA	00FA
024E	F0 0C		BEQ	WINDC	02FB	A5 21		LDA	0021
0250	A5 FA		LDA	FA	02FD	65 FB		ADC	FS
0252	D5 60		CMP,X	0060	02FF	85 FB		STA	00FB
0254	F0 03		BEQ	PLAY	0301	A9 04		LDA	#\$01
0255	4C A5 02	REWIND	JMP	REWIND	0303	85 27		STA	0027
0259	4C D2 02	PLAY	JMP	STOP	0305	A9 85	LOOP4	LDA	#\$FF
025C	A5 FF	WINDC	LDA	FA	0307	8D 07 17		STA	+1024I.T.
0255	D5 60		CMP,X	0060	030A	20 1F 1F	DISPLAY	JSR	SCANDS
0260	F0 F7		BEQ	PLAY	030D	2C 07 17		BIT	I.T.
0262	4C 74 02		JMP	WIND	0310	10 F8		BPL	DISPLAY
0274	18	WIND	CLC		0312	C6 27		DEC	0027
0275	A9 08		LDA	#\$08	0314	D0 EF		BNE	LOOP4
0277	8D 00 17		STA	PAD	0316	20 54 03		JSR	PULL
027A	A5 22		LDA	0022	0319	20 70 03		JSR	STORE
027C	65 FA		ADC	FA	031C	A5 FB		LDA	00FB
027E	85 FA		STA	00FA	031E	D5 70		CMP,X	0070
0280	A5 21		LDA	0021	0320	D0 D3		BNE	PLAY
0282	65 FB		ADC	FB	0322	A5 FF		LDA	00FA
0284	85 FB		STA	00FB	0324	D5 80		CMP,X	0080
0286	A9 01		LDA	#\$01	0326	D0 CD		BNE	PLAY
0288	85 25		STA	0025	0328	A9 04		LDA	#\$04
028A	A9 3E	LOOP 1	LDA	#\$5F	032A	8D 00 17		STA	PAD
028C	8D 07 17		STA	+1024I.T	032D	4C 30 02		JSR	PULL
028F	20 1F 1F	DISPLAY	JSR	SCANDS	0330	EA		NOP	
0292	2C 07 17		BIT	I.T.	0331	A9 0A		LDA	#\$0A
0295	10 F8		BPL	DISPLAY	0333	B5 26		STA	0025
0297	C6 25		DEC	0025	0335	A9 AF	LOOP5	LDA	#\$AF
0299	D0 EF		BNE	LOOP1	0337	8D 07 17		STA	+10 -1I.T
029B	EA EA		NOP		033A	20 1F 1F	DISPLAY	JSR	SCANDS
029D	4C 38 02		JMP	*PULL	0330	2C 07 17		BIT	I.T.
02A5	38	REWIND	SEC		0340	10 F8		BPL	DISPLAY
02A6	A9 01		LDA	#\$01	0342	C6 25		DEC	0025
02A8	8D 00 17		STA	PAD	0344	D0 EF		BNE	LOOP5
02A8	A5 FA		LDA	FA	0346	C8		INY	
02AD	E5 22		SBC	0022	0349	4C 30 02		JMP	BEGIN
02AF	85 FA		STA	00FA	0354	68	PULL	PLA	
02B1	A5 FB		LDA	FB	0355	85 30		STA	0030
02B3	E5 23		SBC	0023	0357	68		PLA	
02B5	85 FB		STA	00FB	0358	85 31		STA	0031
02B7	A9 01		LDA	#\$01	035A	68		PLA	
02B9	85 24		STA	0024	035B	A8		TAY	

INITIALIZE

COMPARE

WIND

REWIND

REWIND (cont'd)

STOP/WAIT

PLAY

SUBROUTINE PULL

Address	Instruction	Label	OP Code	Operand
035C	68		PLA	
035D	AA		TAX	
035E	A5 31		LDA	0031
0360	48		PHA	
0361	A5 30		LDA	0030
0363	48		PHA	
0364	60		RTS	
0370	68	STORE	PLA	
0371	85 30		STA	0030
0373	68		PLA	
0374	85 31		STA	0031
0376	8A		TXA	
0377	48		PHA	
0378	98		TYA	
0379	48		PHA	
037A	A5 31		LDA	
037C	46		PHA	
037D	A5 30		LDA	
037F	48		PHA	
0380	60		RTS	

SUBROUTINE STORE

MEMORY ALLOCATION

Address	Label
0051	Selections 1-15
↓	
005F	Starting Locations High
↓	
0061	Selections 1-15
↓	
006F	Starting Locations Low
↓	
0071	Selections 1-15
↓	
007F	Ending Locations High
↓	
0081	Selections 1-15
↓	
008F	Ending Locations Low
↓	
0000	
↓	
000F	Song Sequence Numbers
↓	
0210	
↓	
0380	Operating Program
↓	
0020	
↓	
0032	Miscellaneous Locations Used Within Main Program

0100

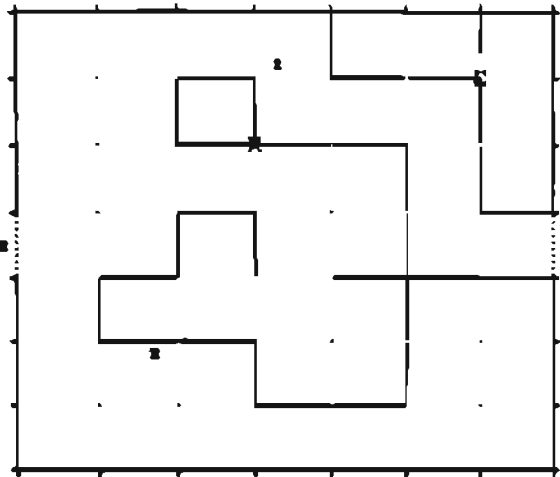
0100 D8 G

	00	01	02	03	04	05	06	07	08	09	0A	0B	0C	0D	0E	0F
0210	F8	18	A9	00	85	20	85	21	85	23	85	F9	85	FA	85	FB
0220	8D	00	17	A9	01	85	22	A9	FF	8D	01	17	A2	00	A0	00
0230	B6	00	20	70	03	4C	3E	02	20	54	03	20	70	03	A5	FB
0240	D5	50	F0	05	10	10	4C	74	02	A9	01	2D	00	17	F0	0C
0250	AS	FA	D5	60	F0	03	4C	A5	02	4C	D2	02	A5	FA	D5	60
0260	F0	F7	4C	74	02	A9	5F	8D	07	17	20	1F	1F	2C	07	17
0270	10	F8	C6	25	18	A9	08	8D	00	17	A5	22	65	FA	85	FA
0280	A5	21	65	FB	85	FB	A9	01	85	25	A9	1E	8D	07	17	20
0290	1F	1F	2C	07	17	10	F8	C6	25	D0	EF	EA	EA	4C	38	02
02A0	EA	EA	EA	EA	EA	3E	A9	01	8D	00	17	A5	FA	ES	22	85
02B0	00	00	00	00	00	00	FB	A9	01	85	24	A9	5F	8D	07	17
02C0	20	1F	1F	2C	07	17	10	F8	C6	24	D0	EF	38	4C	38	02
02D0	18	A5	8A	85	F9	A9	04	8D	00	17	A9	0A	85	26	A9	FF
02E0	8D	07	17	20	1F	1F	2C	07	17	10	F8	C6	26	D0	EF	A9
02F0	02	8D	00	17	18	A5	22	65	FA	85	FA	A5	21	65	FB	85
0300	FB	A9	04	85	27	A9	8F	8D	07	17	20	1F	1F	2C	07	17
0310	10	F8	C6	27	D0	EF	20	54	03	20	70	03	A5	FB	D5	70
0320	D0	D3	A5	FA	D5	80	D0	CD	A9	04	8D	00	17	EA	EA	EA
0330	EA	A9	0A	85	25	A9	AF	8D	07	17	20	1F	1F	2C	07	17
0340	10	F8	C6	25	D0	EF	20	54	03	C8	4C	30	02	A5	30	48
0350	60	37	F1	B8	68	85	30	68	85	31	68	A8	68	AA	A5	31
0360	48	A5	30	48	60	B2	8A	AA	98	98	BA	A8	B2	B2	EA	BE
0370	6E	85	30	68	85	31	8A	48	98	48	A5	31	48	A5	30	48
0380	60															

XIM
0381 36

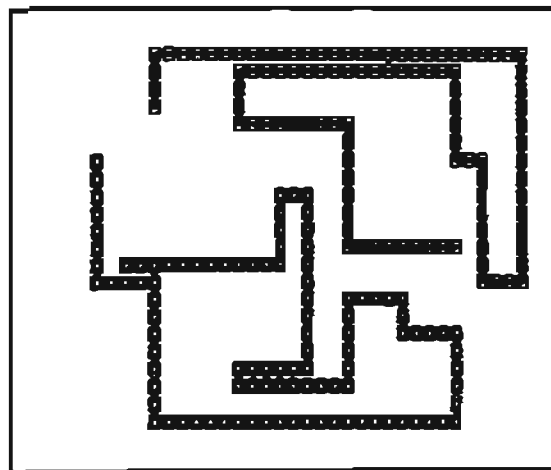
HEX DUMP

Software for the Apple II



SCORE: 108

DYNAMAZE—a dazzling new real-time game. You move in a rectangular game grid, drawing or erasing walls to reflect balls into your goal (or to deflect them from your opponent's goal). Every ball in your goal is worth 100 points, but you lose a point for each unit of elapsed time and another point for each time unit you are moving. Control the speed with a game paddle: play as fast as ice hockey or as slowly and carefully as chess. Back up and replay any time you want to; it's a reversible game. By Don Stone. Integer Basic (plus machine language); 32 K; \$9.95.



SCORE: 105

ULTRA BLOCKADE— the standard against which other versions have to be compared. Enjoy Blockade's superb combination of fast action (don't be the one who crashes) and strategy (the key is accessible open space—maximize yours while minimizing your opponent's). Play against another person or the computer. New high resolution graphics lets you see how you filled in an area—or use reversibility to review a game in slow motion (or at top speed, if that's your style). This is a game that you won't soon get bored with! By Don Stone. Integer Basic (plus machine language); 32 K; \$9.95.

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PERQUACKEY—an exciting vocabulary game which pits the player against the clock. The object of the game is to form words from a group of 10 letters which the computer chooses at random. The words must be 3 to 10 characters in length with no more than 5 words of any particular length. Each player has only 3 minutes per turn. The larger the words the higher the score. Applesoft II 16K; \$9.95.

APPLESHIP—is a naval game in which two players enter their ships in respective oceans. Players take turns trying to blast their opponent's ships out of the water. The first player to destroy their opponent's ships may win the game. A great low-res graphics game. Applesoft II 32K; \$14.95.

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Ask the Doctor

Hints for converting the SYM Tiny PILOT to work on KIM; a Slow Display for the AIM; and, a comparison chart of the AIM, SYM, and KIM expansion pinouts.

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"ASK the Doctor" is intended to be a fairly regular column covering matters of interest to the AIM, SYM and KIM users. Parts I through V may be found in issues 9 (Feb '79) through 13 (June '79). Now that the "Doctor is back from vacation", the column will appear fairly regularly again.

This month we have several topics to cover:

Bob Applegate discusses some problems and solutions to using Tiny PILOT on the KIM.

Thomas M. Walsh provides a short program for use with the AIM to slow down the display when using the disassembler.

The Doctor presents a summary of the Expansion and Application pinouts for the AIM, SYM, and KIM along with a description of the KIM-4 Expansion bus structure.

Tiny PILOT for KIM

Machine language programming is very useful for some applications, but for others it is the long way around. Need to print some data? It is possible, but it is a lot of work. After programming in machine language for a year, I wanted to move up to a high level language such as BASIC. But a BASIC interpreter is not cheap. To make matters worse, most are located from 200016 and up, and my

memory ends at 07FF16. These are two very important facts to consider for any program. I tried writing my own languages but getting a good, small math package was also a major problem. When I saw Tiny Pilot by Nicholas Vrtis (MICRO #16), I was excited! At last I had a neat way to solve some of my programming problems, and to teach some of my non-computer-oriented friends how to program.

Unfortunately, PILOT was written for a SYM, not a KIM. I decided to enter the program, using KIM subroutines in place of SYM subroutines. After entering the program, I started using the interpreter:

```
T: HELLO
S:
@
```

It is a good thing that I don't have a hard-copy terminal because a few feet of paper would have been wasted! Suspecting a mistake in my entry of the interpreter, I checked the program byte-by-byte. Everything was okay. What caused the program to print such garbage? It dawned on me after some thought.

Rereading the last paragraph in Mr. Vrtis' article revealed the answer:

"Tiny PILOT assumes that all registers are preserved by these routines."

Obviously, the KIM monitor does not preserve the registers!

The KIM subroutine OUTCH stores the X register at 00FD, and picks it up again once it is finished. My subroutine SAVOUT (used instead of calls to SYM's OUTCHR) stores the Y register at 00EE, calls OUTCH, reloads the Y register, and exits the routine. SAVIN stores the Y at 00EE, calls GETCH, reloads Y, and exits. SAVCR is a bit longer, because it has to save and restore both registers. It stores Y at the usual place, and X at 00ED. Then it calls CRLF and reloads both registers. Last, but not least, it exits the subroutine.

I located these subroutines in KIM's high RAM, so as to avoid memory problems with Tiny PILOT. Enough room is even left to add a few more statements!

Tiny PILOT is a fun language to use, even if it does have limited capabilities. I hope that some other KIM users will convert between KIM and SYM. I do not know much about SYM's monitor — maybe some MICRO readers could fill me in.

Bob Applegate
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Bordentown, NJ 08505

Expansion Connector					Application Connector				
Computer				Boards	Computer				Boards
PIN	AIM	SYM	KIM	MICRO 65	PIN	AIM	SYM	KIM	MICRO 65
1	SYNC	SYNC	SYNC	GND	1	GND	GND	GND	NC
2	RDY	RDY	RDY	SYNC	2	PA3	PA3	PA3	NC
3	$\phi 1$	$\phi 1$	$\phi 1$	RDY	3	PA2	PA2	PA2	NC
4	IRQ	IRQ	IRQ	IRQ	4	PA1	PA1	PA1	NC
5	S.O.	S.O.	S.O.	S.O.	5	PA4	PA4	PA4	NC
6	NMI	NMI	NMI	NMI	6	PA5	PA5	PA5	NC
7	RES	RES	RES	RES	7	PA6	PA6	PA6	NC
8	DB7	DB7	DB7	DB7	8	PA7	PA7	PA7	NC
9	DB6	DB6	DB6	DB6	9	PB0	PB0	PB0	NC
10	DB5	DB5	DB5	DB5	10	PB1	PB1	PB1	NC
11	DB4	DB4	DB4	DB4	11	PB2	PB2	PB2	NC
12	DB3	DB3	DB3	DB3	12	PB3	PB3	PB3	NC
13	DB2	DB2	DB2	DB2	13	PB4	PB4	PB4	NC
14	DB1	DB1	DB1	DB1	14	PA0	PA0	PA0	NC
15	DB0	DB0	DB0	DB0	15	PB7	PB7	PB7	NC
16	-12V	CS18	K6	NC	16	PB5	PB5	PB5	NC
17	+12V	DBOUT	SSTOUT	NC	17	PB6	Row 0	Row 0	NC
18	CS8	POR	NC	DMA	18	CB1	Col F	Col F	NC
19	CS9	NC	NC	+8V	19	CB2	Col B	Col B	NC
20	CSA	NC	NC	+8V	20	CA1	Col E	Col E	NC
21	+5V	+5V	+5V	+5V	21	CA2	Col A	Col A	NC
22	GND	GND	GND	GND	22	NC	Col D	Col D	NC
A	AB0	AB0	AB0	GND	A	+5V	+5V	+5V	NC
B	AB1	AB1	AB1	AB0	B	NC	CS 00	K0	NC
C	AB2	AB2	AB2	AB1	C	$\phi 2$	CS 04	K1	NC
D	AB3	AB3	AB3	AB2	D	R/W	CS 08	K2	NC
E	AB4	AB4	AB4	AB3	E	Tape 1B-R	CS 0C	K3	NC
F	AB5	AB5	AB5	AB4	F	Tape 1B	CS 10	K4	NC
H	AB6	AB6	AB6	AB5	H	Tape 2B-R	CS 14	K5	NC
J	AB7	AB7	AB7	AB6	J	Tape 2B	CS 1C	K7	NC
K	AB8	AB8	AB8	AB7	K	NC	CS 18	Decode	NC
L	AB9	AB9	AB9	AB8	L	Audio In	Audio In	Audio In	NC
M	AB10	AB10	AB10	AB9	M	Audio Lo	Audio Lo	Audio Lo	NC
N	AB11	AB11	AB11	AB10	N	+12V	RCN-1	+12V	NC
P	AB12	AB12	AB12	AB11	P	Audio Hi	Audio Hi	Audio Hi	NC
R	AB13	AB13	AB13	AB12	R	KBD Rtn	KBD Rtn	KBD Rtn	NC
S	AB14	AB14	AB14	AB13	S	PTR Rtn	PTR Rtn	PTR Rtn	NC
T	AB15	AB15	AB15	AB14	T	KBRD	KBRD	KBRD	NC
U	$\phi 2$	$\phi 2$	$\phi 2$	AB15	U	PTR	PTR	PTR	NC
V	R/W	R/W	R/W	$\phi 2$	V	Tape 2A	Row 3	Row 3	NC
W	R/W	R/W	R/W	R/W	W	Tape 1A	Col G	Col G	NC
X	TEST	TEST	TEST	$\phi 2$	X	NC	Row 2	Row 2	NC
Y	$\phi 2$	$\phi 2$	$\phi 2$	+5V	Y	Serial In	Col C	Col C	NC
Z	Ram R/W	Ram R/W	Ram R/W	GND	Z	NC	Row 1	Row 1	NC

Notes: Signals which are the same are in regular type face. Signals which are different are in bold type face. See your computer manual for a definition of the signals. The MICRO 65 bus is identical to the KIM-4 bus.

Notes: the connections for the application connector are not defined for the MICRO 65 bus. The application connections are defined by the specific requirements of the expansion board and are generally not connected to the host computer.

Slow Down the AIM Display

This program uses AIM subroutines to slow down the display and allows the user to scan thru a disassembly, checking entries made. Holding down the space bar will stop the display at the current display, just as at normal speed, but much more controllably.

After the program is entered into RAM, it is activated by pressing the User F-2 key for Slow Display or the User F-1 key for Normal Speed Display. The User F-3 key is unused and is available for other purposes.

The A, Y, and X registers are pushed onto the stack at 0000 thru 0004. At 0005 and 0008, a JSR is made to the AIM Delay subroutine at E00F, after which X, Y, and A are pulled from the stack and a JMP is made to the Normal Display entry at E005.

The two small sections at 0013 and 001E are used to reset the address which the Monitor points to as the Display Routine: A406, A407. The first subroutine resets the address to Normal Speed, the second sets the address to the Delay routine described above at address 0000, and resets the counter at A417, A418 to FFFF. To speed up the Slow Display, change the value at 0026 to a smaller number, or at address 0005 or 0008 change one of the JSR's to the Delay routine to a NOP.

Thomas M. Walsh
5370 Shafter Avenue
Oakland, CA 94618

<K>*=0

/25

```
0000 48 PHA
0001 98 TYA
0002 48 PHA
0003 8A TXA
0004 48 PHA
0005 20 JSR E00F
0008 20 JSR E00F
000B 68 PLA
000C AA TAX
000D 68 PLA
000E A8 TAY
000F 68 PLA
0010 4C JMP E005
0013 A9 LDA #05
0015 8D STA A406
0018 A9 LDA #EF
001A 8D STA A407
001D 60 RTS
001E A9 LDA #00
0020 8D STA A406
0023 8D STA A407
0026 A9 LDA #FF
0028 8D STA A417
002B 8D STA A418
002E 60 RTS
```

K>*=010C

/2

```
010C 4C JMP 0013
010F 4C JMP 001E
```

AIM, SYM, KIM Pinout Summary

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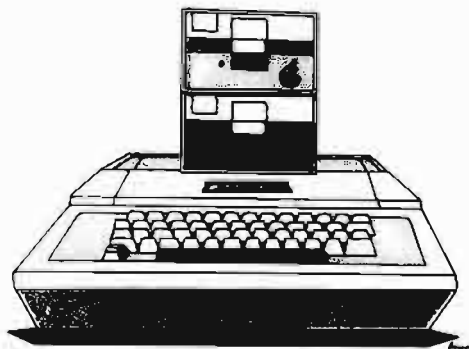
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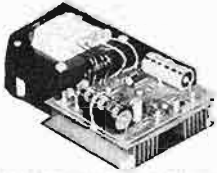
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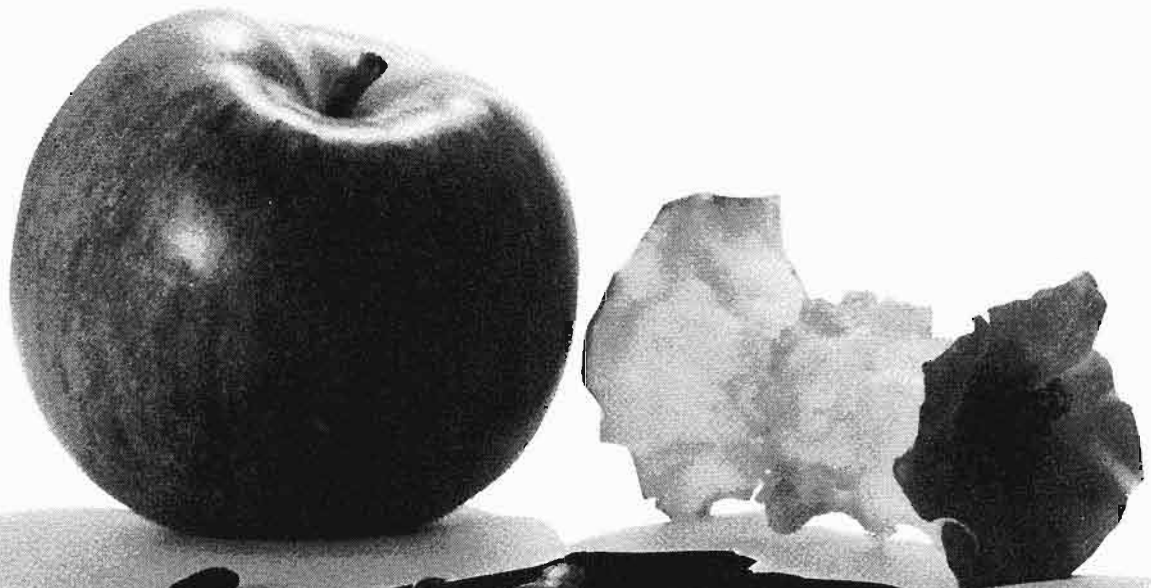
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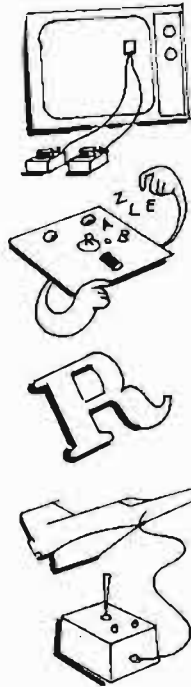
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Graphics and the Challenger C1P, Part 3

Previous articles have discussed fundamentals of the OSI C1P in regards to the polled keyboard and the expanded graphics set. This article shows how to put the pieces together.

William L. Taylor
246 Flora Road
Leavittsburg, OH 44430

In parts one and two of this series we discussed the C1P and some of its features. To be specific, the polled keyboard and the C1P expanded graphics set. An explanation of how to use the polled keyboard and graphics set in some programs written in Basic. The programs that were presented used only one of the many characters that are a part of the 256 characters available in the C1P character generator ROM. This time I would like to continue with the Large Numbers generation and lead up to the twelve hour clock that was promised last time.

Since this is to be a clock program, I will describe this section of the program first. It may seem rather odd to you that the clock mainline program is buried in the program, but this is how the program evolved. Primarily most of the number generating routines were developed first due to the past part of this series. This is not the best way to write a program, but some programs do evolve in this manner.

The clock mainline routine was a separate program and this portion will be described as a single unit that can be used without the large graphic characters for some of the users that do not have the amount of memory required for the whole program. The clock with the numerals is extremely long. It occupies nearly eight K of user memory. For those users that do not have enough memory to run the entire program I hope that you will use the number generating routines in some of your own programs that would require such things as hit scores or other number displays.

Some beginning criteria for a clock must be given at this point. Any clock that has a digital display must have a number set. The number set must have at least a minimum of four digits of display to qualify as a working clock. Also the hours and minutes must be separate entries. That is, we must have a means of separating the hours and minutes. In addition, we must also have a method of setting the clock to the right time before starting the clock. Finally, we must update the time at some interval. This is usually at one-minute or one-second intervals. The clock should also have a period of day indicator, such as AM or PM.

With this in mind, let's examine the clock portion of our main line Basic program routine that is located at Lines 4000 through 4070. This part of the program will be described in detail and the modifications that are required to make it independent from the rest of the program will be given. Looking at the beginning of Line 4000 we see that a GOSUB is executed. The subroutine at line 2900 through 3030 is the fast screen erase machine code memory load routine. This machine code routine will be called to clear the screen for every update of the display. The subroutine is used with both versions of the clock. An explanation of the subroutine was given in part two of this series and the reader is referred to this part for a complete description (MICRO 19:61).

When the program returns from the fast screen routine, the clock must be set

to the correct time. This is hours, minutes and seconds where you wish for your clock to start. When you hit a carriage return, the clock begins to run and will be updated on the next whole minute. The hours are contained in the variable S. The minutes are contained in the variable R, and the seconds are contained in the variable Z. The variables are at lines 4004, 4006 and 4007. The actual timer for the clock is a FOR-NEXT loop established at lines 4008 and 4010. This loop should be adjusted to insure accurate timing of your clock. To have the clock run faster, decrease the value of the variable I at line 4008. To decrease the clock rate, increase the value of the variable I at line 4008. After the loop at lines 4008 and 4010 has timed out, the program falls through to the next line. At line 4011 the variable Z is checked to see if a complete minute has been reached ($Z = 60$). If Z does not = 60 then the timing loop is re-established. When Z is equal to 60, or one minute, the minute counter at line 4013 is incremented. Next at line 4014, a GOSUB to line 4030 resets the second counter to zero. At line 4015 a GOSUB to line 4059 will execute the fast screen erase routine and clear the monitor screen. During this subroutine at lines 4059 through 4065, we will go and check to see what numerals are to be displayed from the hours and minutes look-up tables at lines 59 through 390. It is in these tables that the variables S and R (hours and seconds) are determined and an equivalent numerical display is generated on the monitor screen. When the program returns to the clock mainline program at line 4016, the R variable is checked to see if 60 minutes

has been reached. If 60 minutes has not been reached as compared at line 4016, then a new pass through the program is executed. If 60 minutes has been reached (R = 60), then the hours counter will be incremented (variable S). Next, at line 4018 a GOSUB to line 4032 will reset the minute counter and the screen is cleared. A new pass through the look-up table is executed and a new time update is displayed on the monitor screen. At line 4019, the S variable or hours is checked to see if 13 hours has elapsed. We must display 12 hours and 59 minutes. If the S variable does not equal 13, a new pass through the program is executed. If the variable S is equal to 13 or full hours counter, a GOSUB to line 4034 will cause the Z variable to be reset. At line 4035, the R variable is reset to zero. At line 4036, the hours counter (S variable) is reset and a GOSUB to line 4059 will clear the monitor screen. The display is updated to 1:00 o'clock and a new pass through the program is executed at line 4037. What all this says is that for each minute that the clock runs, there will be a correct time displayed. For every minute, there will be a new time-up date.

As stated before, the clock routine can be used independent of the whole program. The reader can use this explanation of the routine and the separate program in Listing 2 as a separate program. This listing differs from the routine just described in that it uses a PRINT statement to give the user a viewable readout. Also, this program will update the time every second. If you do not have sufficient memory for the complete numerical clock, please try the smaller version on your C1P.

In the last part of this series we discussed how the large numerals were generated. In fact, some of the large numeral routines are included in this article. At this point, we will continue with the graphics generation and discuss how these subroutines are used in the program for our clock. The contents of Table 1 lists the line numbers of the key subroutines begin. The reason that we tabulate these subroutines instead of identifying them in the Basic program is the fact that the Rem statements will occupy memory, and we need to conserve in order to fit the program in 8K of user memory.

Included with this article is a C1P video memory map that shows the complete video memory as related to the monitor screen. This memory map is in decimal. The locations for the large numbers are shown. These digits will appear at these locations on the monitor screen. With this chart and the number subroutines in the program, you can write programs of your own that require any number displays.

Table 1: Numerical Clock routines

Line	Description	Count
60 to 385	Numerical look up tables	
1000 to 1020	Least significant digit	One
1100 to 1190	Least significant digit	Two
1200 to 1280	Least significant digit	Three
1300 to 1360	Least significant digit	Four
1400 to 1460	Least significant digit	Five
1500 to 1570	Least significant digit	Six
1600 to 1640	Least significant digit	Seven
1700 to 1760	Least significant digit	Eight
1800 to 1890	Least significant digit	Nine
2000 to 2070	Least significant digit	Zero
2900 to 3030	Fat screen ML load routine	
4000 to 4070	Clock main line program	
5000 to 5080	Second most digit	Zero
5100 to 5120	Second most digit	One
5200 to 5230	Second most digit	Two
5300 to 5340	Second most digit	Three
5400 to 5425	Second most digit	Four
5500 to 5535	Second most digit	Five
5600 to 5635	Second most digit	Six
5700 to 5710	Colon separator for hours and minutes	
6000 to 6025	Third most digit	Zero
6100 to 6130	Third most digit	One
6200 to 6235	Third most digit	Two
6300 to 6335	Third most digit	Three
6400 to 6430	Third most digit	Four
6500 to 6535	Third most digit	Five
6600 to 6645	Third most digit	Six
6700 to 6720	Third most digit	Seven
6800 to 6835	Third most digit	Eight
6900 to 6935	Third most digit	Nine
7000 to 7010	Most Significant digit	One

Table 2: Alarm option program changes

```

2 X = 63232
3 POKE X + 1,0: POKE X + 3,0: POKE X,255: POKE X + 2,0
4 POKE X + 1,4: POKE X + 3,4
5 POKE X,0
6 GOSUB 4000
4003 INPUT 'SET ALARM'; B,C: D=C + 2
4010 NEXT I
4011 Z=Z + 1: GOSUB 8007
4063 GOSUB 8005
8000 REM ALARM TEST
8005 IF B=S AND C=R THEN POKE X,1
8006 RETURN
8007 REM TURN OFF ALARM PRESS 1 KEY
8008 G=57088
8009 POKE 530,1
8010 POKE G, 127
8015 IF PEEK (G)=127 THEN POKE X,0
8020 POKE 530,0
8025 RETURN

```

It must be explained at this point that there are subroutines that generate the Least Significant Digits 0 through 9; the Second Most Digits 0 through 6; the Third Most Digits 0 through 9, and finally, the Most Significant Digit 1. The combination of these subroutines together will generate a display of the time. As an example, say the time 12:30 was contained in the S and R variables, we would need to generate digits for four characters. These would be the Most Significant digit one; the Third Most digit two; the Second Most Digit three; and finally, the Least Significant Digit zero. If the variable S contained 12 and the variable R contained 30, when the program goes through to look up tables, variable R would be compared to 30. When 30 was found at Line 215, a GOSUB to Lines 2000 and 5300 would result in the generation of a Second Most digit 3 and a Least Significant digit 0 to be displayed on the screen. Also, when the value for the variable S is found in the look-up table at Line 385, a GOSUB to Lines 6200 and 7000 will cause the generation and display of the Most Significant digit 1 and the Third Most digit 2. From the example, it can be seen that when we are generating a digit display there are usually more than one of the subroutines used to create the graphics.

In the last part of this series, I explained how one example subroutine worked to generate a large number graphic display. The demonstration program in the last part of this series contained subroutines to generate the Least Significant Digits that are a part of this article. Although I described one subroutine in the last part, I will give a description of how one of the subroutines works in this article. The reader may not have the last issue that contained the article, so a description of the number subroutines will make this article a complete entry.

Lets take one subroutine that is used to generate the large numerals and briefly describe its operation. Take the graphics character that represents the numeral 1 in the Least Significant digit location. This subroutine is located at Line 1000 through 1020. First, we must define the locations on our C1P monitor screen that we wish to start to place our character. In the subroutine we are using, the variable A as the video memory pointer. You can see that variable A was defined as video memory locations 54000 to 54128 decimal. This sets up our boundaries in video memory where we wish to place our character. This statement forms part of a FOR-NEXT loop that will be used to load the character that creates the display on the monitor screen. Also note in the statement at Line 1000 we have used a function called the STEP function. This function in a statement will cause the variable to be incremented by the amount contain-

ed in the STEP value. In this instance we wish to increment the A variable by 32 for each pass through the loop in the statement line. At the next statement line, the decimal equivalent of a white square will be placed at decimal location 54000. This will be the first part of the data in video RAM that will make up our number character. At the next statement line the program returns to the first line where our FOR-NEXT loop began.

The A variable will be incremented by 32, and the program will fall through the loop again. At the next statement line another square will be placed in video RAM and displayed on the monitor screen. This process will continue until the A variable has been incremented to the final value set in line 1000. This is 54128 decimal. We will now have the graphics representation of the numeral 1 displayed on the monitor screen. With this explanation of the subroutine for the graphics figure 1, you should be able to analyze the remainder of the subroutines to understand them more clearly.

I have written the program to display the large numerals near the bottom left corner of the C1P's monitor screen. If the user should wish these characters displayed at a different location, they can be relocated. This is not a simple task but can be done with the aid of the video memory map that is included as part of this article. From the memory map determine the locations where you wish to have the characters displayed and change the decimal addresses to correspond to the new locations. If you are going to use the number routines for other programs, this may be necessary; but with the clock program as written, remember that the fast screen erase routine will clear only the bottom half of the monitor screen. If you relocate the graphics characters, you will need to have your fast screen erase routine clear the location where you have located your display.

This program is written in subroutines as stated before. In addition to the separate clock and subroutines for the numbers, the fast screen erase routine can be used in other programs that may require this feature. This could be for a rapid screen erase for animated games. The subroutines have many usages even if you cannot run the entire program on your machine.

Basically, this article was written for an OSI Challenger C1P; but the programs will run on other OSI computers with some changes. I have not included these changes in this article because OSI systems are somewhat different. If you have BASIC, you can modify the program to suit your video output such as the 540 in the C2-4P. In addition, a separate listing for an alarm option is included for

those users who should have a PIA port in their Challengers. Please refer to Table 2 for the list of the program changes required for the alarm option. The user will need a tone device to implement this option. The alarm option uses a 6820 PIA located at F700 HEX. The A side of the port is used and PAO is the specific port.

When using either version of the clock, the user must set memory size to protect the machine code routine that is stored in user memory. When using the complete graphics and clock program, the user must set memory size to 8167. When using the shortened version, set memory size to 3840 decimal. When using the clock for either version, the clock timing loop will have to be adjusted for your system to insure accuracy. The clock is tied to the Challenger Processor clock and differs depending on the program being used.

In conclusion, although the BASIC clock requires much memory and will not have the accuracy of a hundred dollar quartz watch, it can be a fine demonstrator. The primary purpose of this article was to describe the C1P's features and teach some programming techniques that could be used by the readers for other programs. This article and programs cover many of the features of BASIC and the Challenger C1P in general. I hope that I have helped some readers and users of the OSI C1P and other OSI systems to grasp a better understanding of BASIC and the graphics capabilities of these fine machines. In the next part of this series, I will show how to do some plotting and create some animated characters using BASIC. Until then, good luck!!

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39 505UB 4059
59 505UB 5700
60 IF R=0 THEN 505UB 2800:505UB 5800
70 IF R=1 THEN 505UB 1000:505UB 7000
80 IF R=2 THEN 505UB 1100:505UB 5000
90 IF R=3 THEN 505UB 1200:505UB 5000
100 IF R=4 THEN 505UB 1300:505UB 5000
110 IF R=5 THEN 505UB 1400:505UB 5000
120 IF R=6 THEN 505UB 1500:505UB 5000
130 IF R=7 THEN 505UB 1600:505UB 5000
140 IF R=8 THEN 505UB 1700:505UB 5000
150 IF R=9 THEN 505UB1800:505UB 5000
160 IF R=10 THEN 505UB 5100:505UB 2000
165 IF R=11 THEN 505UB 5100:505UB 1000
175 IF R=12 THEN 505UB 1100:505UB 5100
190 IF R=13 THEN 505UB 1200:505UB 5100
192 IF R=14 THEN 505UB 1300:505UB 5100
194 IF R=15 THEN 505UB 1400:505UB 5100
196 IF R=16 THEN 505UB 1500:505UB 5100
197 IF R=17 THEN 505UB 1600:505UB 5100
200 IF R=18 THEN 505UB 1700:505UB 5100
202 IF R=19 THEN 505UB 1800:505UB 5100
205 IF R=20 THEN 505UB 2000:505UB 5200
205 IF R=21 THEN 505UB 1000:505UB 5200
207 IF R=22 THEN 505UB 1100:505UB 5200
208 IF R=23 THEN 505UB 1200:505UB 5200
209 IF R=24 THEN 505UB 1300:505UB 5200
210 IF R=25 THEN 505UB 1400:505UB 5200
211 IF R=26 THEN 505UB 1500:505UB 5200
212 IF R=27 THEN 505UB 1600:505UB 5200
213 IF R=28 THEN 505UB 1700:505UB 5200
214 IF R=29 THEN 505UB 1800:505UB 5200
215 IF R=30 THEN 505UB 2000:505UB 5200
216 IF R=31 THEN 505UB 1000:505UB 5300
217 IF R=32 THEN 505UB 1100:505UB 5300
218 IF R=33 THEN 505UB 1200:505UB 5300
219 IF R=34 THEN 505UB 1300:505UB 5300
220 IF R=35 THEN 505UB 1400:505UB 5300
221 IF R=36 THEN 505UB 1500:505UB 5300
222 IF R=37 THEN 505UB 1600:505UB 5300
223 IF R=38 THEN 505UB 1700:505UB 5300
224 IF R=39 THEN 505UB 1800:505UB 5300
225 IF R=40 THEN 505UB 2000:505UB 5400
226 IF R=41 THEN 505UB 1000:505UB 5400
227 IF R=42 THEN 505UB 1100:505UB 5400
228 IF R=43 THEN 505UB 1200:505UB 5400
229 IF R=44 THEN 505UB 1300:505UB 5400
230 IF R=45 THEN 505UB 1400:505UB 5400
231 IF R=46 THEN 505UB 1500:505UB 5400
232 IF R=47 THEN 505UB 1600:505UB 5400
233 IF R=49 THEN 505UB 1800:505UB5400
234 IF R=50 THEN 505UB 2000:505UB 5500
235 IF R=51 THEN 505UB 1000:505UB5500
236 IF R=52 THEN 505UB 1100:505UB 5500
237 IF R=53 THEN 505UB 1200:505UB 5500
238 IF R=54 THEN 505UB 1300:505UB 5500
239 IF R=55 THEN 505UB 1400:505UB 5500
240 IF R=56 THEN 505UB 1500:505UB 5500
241 IF R=57 THEN 505UB 1600:505UB 5500
242 IF R=58 THEN 505UB 1700:505UB 5500
243 IF R=59 THEN 505UB1800:505UB 5500
245 IF R=48 THEN 505UB 1700:505UB 5400
246 RETURN
300 IF S=0 THEN 505UB 6000
310 IF S=1 THEN 505UB 6100
320 IF S=2 THEN 505UB 6200
330 IF S=3 THEN 505UB 6300
340 IF S=4 THEN 505UB 6400
350 IF S=5 THEN 505UB 6500
355 IF S=6 THEN 505UB 6600
360 IF S=7 THEN 505UB 6700
365 IF S=8 THEN 505UB 6800
370 IF S=9 THEN 505UB 6900
375 IF S=10 THEN 505UB 6000:505UB 7000
380 IF S=11 THEN 505UB 6100:505UB 7000
385 IF S=12 THEN 505UB 6200:505UB7000

```



```

390 RETURN
1000 FOR A=54000 TO 54128 STEP 32
1010 POKE A,161:NEXT A
1020 RETURN
1100 FOR A= 54000 TO 54002 STEP 1
1110 POKE A,161:NEXT A
1120 POKE 54034,161
1140 FOR A=54064 TO 54066 STEP 1
1150 POKE A,161:NEXT A
1160 POKE 54096,161
1170 FOR A= 54128 TO 54130 STEP 1
1180 POKE A,161:NEXT A
1190 RETURN
1200 FOR A= 54000 TO 54002 STEP 1
1210 POKE A,161:NEXT A
1220 POKE 54034,161
1230 FOR A=54064 TO 54066 STEP 1
1240 POKE A,161:NEXT A
1250 POKE 54096,161
1260 FOR A=54128 TO 54130 STEP 1
1270 POKE A,161: NEXT A
1280 RETURN
1300 FOR A =54000 TO 54064 STEP 32
1310 POKE A , 161: NEXT A
1320 FOR A=54064 TO 54066 STEP 1
1330 POKE A,161:NEXTA
1340 FOR A=54002 TO 54130 STEP 32
1350 POKE A,161: NEXT A
1360 RETURN
1400 FORA=54000 TO 54002 STEP1
1400 FORA=54000 TO 54002 STEP1
1410 POKE A,161: NEXT A
1420 FOR A=54064 TO 54066 STEP 1
1425 POKE A,161:NEXT A
1430 FOR A=54128 TO 54130 STEP 1
1440 POKE A,161: NEXT A
1450 POKE 54032,161: POKE 54096,161
1460 RETURN
1500 FOR A=54000TO54002 STEP 1
1510 POKE A, 161: NEXT A
1520 FOR A=54064 TO 54066 STEP 1
1530 POKE A, 161: NEXT A
1540 FOR A=54128 TO 54130 STEP 1
1550 POKE A, 161:NEXT A
1560 POKE 54032,161:POKE 54096,161:
      POKE 54098,161
1570 RETURN

1600 FOR A=54000 TO 54002 STEP 1
1610 POKE A, 161: NEXT A
1620 FOR A=54002 TO 54130 STEP 32
1630 POKE A,161:NEXT A
1640 RETURN

1700 FOR A=54000 TO 54128 STEP 32
1710 POKE A,161:NEXT A
1720 FOR A= 54002 TO 54130 STEP 32
1730 POKE A, 161: NEXT A
1740 FOR A=54001 TO 54129 STEP 64
1750 POKE A, 161: NEXT A
1760 RETURN
1800 FOR A=54002 TO 54130 STEP 32
1810 POKE A, 161: NEXT A
1820 FOR A= 54000 TO 54002 STEP 1
1830 POKE A, 161: NEXT A
1840 FOR A =54064 TO 54066 STEP 1
1850 POKE A, 161: NEXT A
1860 FOR A=54128 TO 54130 STEP 1
1870 POKE A, 161: NEXT A
1880 POKE 54032, 161
1890 RETURN
1900 FOR A=53998 TO 54126 STEP 32
1910 POKE A, 161: NEXT A
1930 RETURN
2000 FOR A= 54000 TO 54002 STEP 1
2010 POKE A,161:NEXT A
2020 FOR A=54000 TO 54128 STEP 32
2030 POKE A, 161: NEXT A
2040 FOR A= 54002 TO 54130 STEP 32
2050 POKE A, 161:NEXT A
2060 POKE 54129, 161
2070 RETURN
2900 FOR R=8168 TO 8191
2920 READ F:POKE R,F:NEXT R
2925 RESTORE
2930 RETURN
3000 DATA 169,32,160,4,162,0,157,0
3010 DATA 210,232,208,250,238,240
3020 DATA 31,136,208,244,169,210
3030 DATA 141,240,31,96

4000 GOSUB 2900
4002 PRINT" TIME HRS SEC MIN"
4004 INPUT S
4006 INPUT R
4007 INPUT Z
4008 FOR I=1 TO 725
4010 NEXT I
4011 Z=Z+1
4012 IF Z<60 THEN 4008
4013 IF Z=60 THEN R=R+1
4014 IF Z=60 THEN GOSUB 4030
4015 GOSUB 4059
4016 IF R<60 THEN GOTO 4008
4017 IF R=60 THEN S=S+1
4018 IF R=60 THEN GOSUB 4032
4019 IF S<13 THEN 4008
4020 IF S=13 THEN 4034

```

```

4030 Z=0
4031 RETURN
4032 R=0:GOSUB 4059
4033 RETURN
4034 Z=0
4035 R=0
4036 S=1:GOSUB 4059
4037 GOTO 4098
4053 POKE 11,232:POKE 12,31
4054 GOTO 5
4059 POKE 11,232:POKE 12,31:X=USR(X)
4060 GOSUB 300
4062 GOSUB 59
4065 RETURN
5000 FOR A=53996 TO 53998 STEP 1
5010 POKE A,161:NEXT A
5020 FOR A=54124 TO 54126 STEP 1
5030 POKE A,161:NEXT A
5040 FOR A=53996 TO 54124 STEP 32
5050 POKE A,161:NEXT A
5060 FOR A=53998 TO 54126 STEP 32
5070 POKE A,161:NEXT A
5080 RETURN
5100 FOR A=53998 TO 54126 STEP 32
5110 POKE A,161:NEXT A
5120 RETURN
5200 FOR A=53996 TO 53998 STEP 1
5205 POKE A,161:NEXT A
5210 FOR A=54124 TO 54126 STEP 1
5215 POKE A,161:NEXT A
5220 POKE 54092,161:POKE54030,161
5230 FOR A=54060 TO 54062 STEP 1
5235 POKE A,161:NEXT A
5240 RETURN
5300 FOR A=53996 TO53998 STEP 1
5300 FOR A=53996 TO53998 STEP 1
5305 POKE A,161:NEXT A
5310 FOR A=54060 TO 54062 STEP 1
5315 POKE A,161:NEXT A
5320 FOR A=54124 TO 54126 STEP 1
5325 POKE A,161:NEXT A
5330 POKE 54030,161:POKE 54094,161
5340 RETURN
5400 FOR A=53998 TO 54126 STEP 32
5405 POKE A,161:NEXT A
5410 FOR A=53996 TO 54060 STEP 32
5415 POKEA,161:NEXT A
5420 POKE 54061,161
5425 RETURN
5500 FOR A=53996 TO 53998 STEP 1
5505 POKE A,161:NEXT A
5510 FOR A=54124 TO 54126 STEP 1
5515 POKE A,161:NEXT A
5520 FOR A=54060 TO 54062 STEP 1
5525 POKE A,161:NEXT A
5530 POKE 54028,161:POKE 54062,
      161:POKE 54094,161
5535 RETURN
5600 FOR A=53996 TO 53998 STEP 1
5605 POKE A,161:NEXT A
5610 FOR A=54060 TO 54062 STEP 1
5615 POKE A,161:NEXT A
5620 FOR A=54124 TO 54126 STEP 1
5625 POKE A,161:NEXT A
5630 POKE 54092,161:POKE54094,
      161:POKE 54028,161
5635 RETURN
5700 POKE 54027,172:POKE54091,172
5710 RETURN
6000 FOR A=53992 TO 54120 STEP 32
6005 POKE A,161:NEXT A
6010 FOR A=53994 TO 54122 STEP 32
6016 POKE A,161:NEXT A
6020 POKE 53993,161:POKE 54121,161
6025 RETURN
6100 FOR A=53994 TO 54122 STEP 32
6120 POKE A,161:NEXT A
6130 RETURN
6200 FOR A=53992 TO 53994
6205 POKE A,161:NEXT A
6210 FOR A=54056 TO 54058
6215 POKE A,161:NEXT A
6220 FOR A=54120 TO 54122
6225 POKE A,161:NEXT A
6230 POKE 54026,161:POKE 54088,
      161:POKE54057,161
6235 RETURN
6300 FOR A=53992 TO 53994
6305 POKE A,161:NEXT A
6310 FOR A=54056 TO 54058
6315 POKE A,161:NEXT A
6320 FOR A=54120 TO 54122
6325 POKE A,161:NEXT A
6330 POKE 54026,161:POKE 54090,161
6335 RETURN
6400 FOR A=53994 TO 54122 STEP 32
6405 POKE A,161:NEXT A
6410 FOR A=54056 TO 54058
6415 POKE A,161:NEXT A
6420 FOR A=53992 TO 54056 STEP 32
6425 POKE A,161:NEXT A
6430 RETURN
6500 FOR A=53992 TO 53994
6505 POKE A,161:NEXT A
6510 FOR A=54056 TO 54058
6515 POKE A,161:NEXT
6520 FOR A=54120 TO 54122
6525 POKE A,161:NEXT A
6530 POKE 54024,161:POKE 54092,161

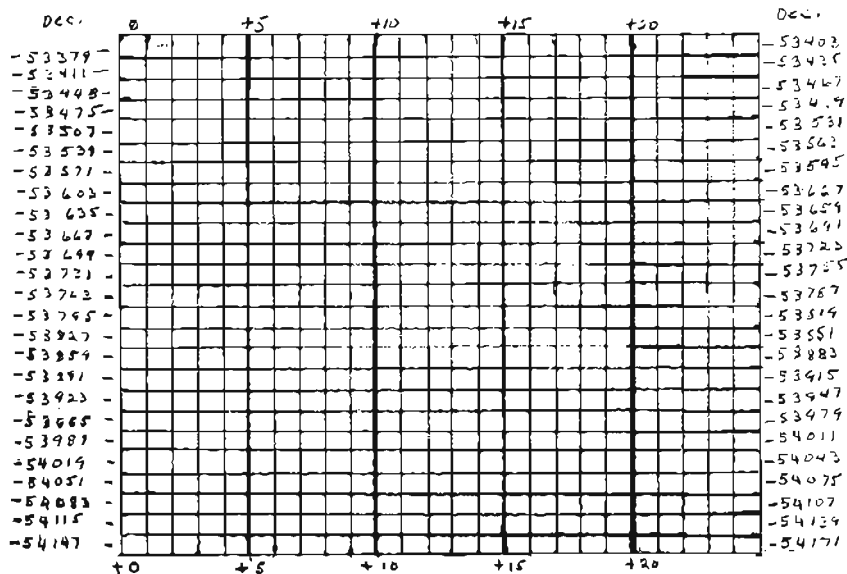
```

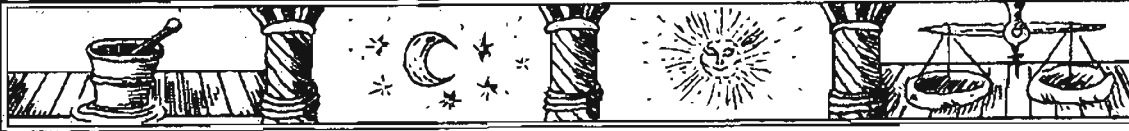
```

6535 RETURN
6600 FOR A=53992 TO 54120 STEP 32
6605 POKE A,161:NEXT A
6610 FOR A=53992 TO 53994
6615 POKE A,161:NEXT A
6620 FOR A=54120 TO 54122
6625 POKE A,161:NEXT A
6630 FOR A=54056 TO 54058
6635 POKE A,161:NEXT A
6640 POKE 54098,161
6645 RETURN
6700 FOR A=53992 TO 53994
6705 POKE A,161:NEXT A
6710 FOR A=53994 TO 54122 STEP 32
6715 POKE A,161:NEXT A
6720 RETURN
6800 FOR A=53994 TO 54120 STEP 32
6805 POKE A,161:NEXT A
6810 FOR A=53992 TO 54120 STEP 32
6815 POKE A,161:NEXT A
6820 POKE 53998,161:POKE 54007,
      161:POKE 54121,161
6825 RETURN
6900 FOR A=53994 TO 54122 STEP 32
6905 POKE A,161:NEXT A
6910 FOR A=54056 TO 54058
6915 POKE A,161:NEXT A
6920 FOR A=53992 TO 53994
6925 POKE A,161:NEXT A
6930 POKE 54024,161
6932 FOR A=54122 TO 54120 STEP -1
6934 POKE A,161:NEXT A
6935 RETURN
7000 FOR A=53999 TO 54113 STEP 32
7005 POKE A,161:NEXT A
7010 RETURN
2 PRINT" ENTER TIME HRS MIN SEC"
3 GOTO 50
4 INPUT C
6 INPUT B
7 INPUT A
8 FOR I=1 TO 400
10 NEXT I
11 GOSUB 60
12 A=A+1
13 IF A<60 THEN GOTO 8
14 IF A=60 THEN B=B+1
15 IF A=60 THEN GOSUB 30
16 IF B<60 THEN GOTO 8
17 IF B=60 THEN C=C+1
18 IF B=60 THEN GOSUB 32
19 IF C<13 THEN GOTO 8
20 IF C=13 THEN GOTO 34
30 A=0
31 RETURN
32 B=0
33 RETURN
34 A=0
35 B=0
36 C=1
37 GOTO 8
49 REM SET MEMORY SIZE TO 4050
50 FOR Q=4072 TO 4095
51 READ N:POKE Q,N
52 NEXT Q
53 POKE 11,232:POKE 12,15
54 GOTO 4
60 X=USR (X)
61 PRINT" TIME";C;" ";B;" ";A
62 RETURN
70 DATA 169,32,160,8,162,0,157,0
75 DATA 200,232,200,250,238,240
80 DATA 15,136,200,244,169,200
85 DATA 141,240,15,96

```

C1P Memory Map in decimal 25 x 25 format





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The MICRO Software Catalogue: XVII

Mike Rowe
P.O. Box 6502
Chelmsford, MA 01824

Name: **TXT/ED 2.0**
System: **APPLE II**
Memory: **32K RAM with ROM Applesoft, or 48K RAM (disk) Applesoft**
Language: **APPLESOFT and Machine Language**
Hardware: **APPLE II, Disk II (A printer with Serial or Parallel Interface is desirable)**

Description: TXT/ED is a disk-based Word Processor and an APPLESOFT BASIC program editor. Major features of the TXT/ED 2.0 include: no confusing CONTROL characters within your text, full right margin justification, merging of multiple disk files, find or change any text sequence in text memory, fully supported upper and lower case letters, extensive Text Formatting capabilities (including text lines, page numbers, two column print format), full data display (including page scrolling), slow-list and stop-list display of text data, conversion of APPLESOFT programs to text form for editing, then reconversion back to run-time format, selective saving of all or part of text memory to disk, multiple Disk II fully supported, easy creation of APPLE DOS 'EXEC' files, up to nine Tab Stops may be set for columnar data, line or paragraph block move, duplicate and center. Easy interfacing to any type printer.

Copies: **Just Released**
Price: **\$65.00 on disk**
Includes: **System disk, 51 page instruction manual**
Author: **Gerald H. Rivers**
Available: **G.H. Rivers
P.O. Box 833
Madison Heights, MI
48071**

Name: **ISAM—DS**
System: **APPLE II**
Memory: **3K plus index table storage**
Language: **Applesoft**
Hardware: **APPLE II, Disk II**

Description: ISAM—DS is an integrated set of fifteen utility routines that facilitate the creation and manipulation of indexed files. Records on indexed files may be easily and quickly retrieved, either directly (randomly) or in sequence. Each record is identified by a key data value. The key values do not have to be part of the record; they do not have to be unique to each record; and partial key values may be used in retrieving records. The interface between ISAM—DS and an Applesoft program is through a single entry point (GOSUB) and nine variables. Files can be created, opened, closed, copied, and erased. Records can be written, read, changed, and deleted. File space that is freed by deleting a record is automatically reused when another record is added. There is never a need to "clean up" a file because of update activity. ISAM—DS is a must for writing business systems for the APPLE II and is equally useful in personal programs or for learning index-sequential file processing techniques.

Copies: **Just Released**
Price: **\$50.00 (Texas residents add 5 percent sales tax.)**
Includes: **Integrated set of routines, documentation for the routines, and a sophisticated mailing list program that demonstrates ISAM—DS capabilities. Append routines for DOS 3.1 and 3.2 are also included. The**

append routines are used to join the ISAM—DS package to an Applesoft program.
Author: **Robert F. Zant**
Available: **Decision Systems
P.O. Box 13006
Denton, TX 76203**

Name: **COMMODITY FILE**
System: **APPLE II Computer**
Memory: **2K with Applesoft ROM 48K with Applesoft RAM**
Language: **APPLESOFT II**
Hardware: **Disk II, 132 column printer (optional)**

Description: Commodity File stores and retrieves virtually every commodity traded on all Future's exchanges. A self-prompting program allowing the user to enter short/long contracts. Computes gross and net profits/losses, and maintains a running cash balance. Takes into account any amending of cash balances such as new deposits or withdrawals from the account. Instantaneous readouts (CRT or printer) of contracts on file, cash balances, P/L statement. Includes color bar graphs depicting cumulative and individual transactions. Also includes routine to proofread contracts before filling.

Copies: **60plus**
Price: **\$19.95 Diskette plus \$1.95 P&H, First Class, Check or money order.**
Includes: **System diskette and full documentation.**
Author: **S. Goldstein**
Available: **Mind Machine, Inc.
31 Woodhollow Lane
Huntington, N.Y. 11743**

Copyrighted: 3/1/79, all rights reserved.

Name: **Astronomy Software**
System: **PET**
Memory: **8K or more**
Language: **BASIC**

Description: Astronomical programs for PET; Time, coordinate, and compass direction of celestial objects. These and many other programs for PET by JAPS —Jacksonville Area Pet Society.

Copies: **Hundreds**
Price: **\$1.50 per program, plus \$1.00 for tape and postage.**
Includes: **Cassette**
Available: **Send self-addressed stamped envelope to:**
Pet Library
401 Monument Rd. No. 123
Jacksonville, FL 32211

Name: **TRAP65**
System: **Any 6502 based microcomputer**
Memory: **Not applicable**
Language: **Not applicable**

Description: TRAP65 is a hardware device which plugs into the 6502 microprocessor's socket. TRAP65 monitors each opcode that the 6502 executes; and if an unimplemented opcode is about to be executed, a BRK instruction is forced on the data bus. This prevents system crashes especially when debugging machine language programs. TRAP65 can also be used to extend the 6502 instruction set. For example, OF is an unimplemented opcode that can, via appropriate routine, become a PHX (push X) instruction or any function that you can define in software.

Copies: **Just released.**
Price: **\$149.95**
Authors: **J. R. Hall and C. W. Moser**
Available: **Eastern House Software**
3239 Linda Drive
Winston-Salem, N.C. 27108

Name: **Applesoft Tape Verifier**
System: **Apple II or Apple II Plus**
Memory: **16K RAM**
Language: **Applesoft**

Description: This program gives the Apple computer the capability of verifying Applesoft programs that have been saved out on tape. It does this without destroying the original program. The program will work with either the Apple II or the Apple II Plus computers and will also work with either RAM or ROM Applesoft.

Copies: **Just released.**
Price: **\$20.00**
Available: **Softsell Associates**
2022 79th Street
Brooklyn, N.Y. 11214

Name: **Mailing List Database**
System: **APPLE II**
Memory: **48K**
Language: **Applesoft**
Hardware: **Applesoft on ROM and at least one disk drive.**

Description: This new, user oriented mailing list program introduces professional quality and speed to the processing of name and address files. Labels on printed lists can be readily produced at any time. Features include: single keystroke commands, convenient data entry, machine language searches, machine language sorts, flexible application and versatile output. Mailing List Database is supplied on disk and comes with a program for automatically converting existing text mailing list files. It requires 48K Apple II with Applesoft on Rom (or language card) and at least one disk drive.

Copies: **Many**
Price: **\$34.50 (WA residents add 5.3 percent sales tax).**
Authors: **Robert C. Clardy and Christopher Anson**
Available: **Synergistic Software**
5221 - 120th Avenue, S.E.
Bellevue, WA 98006

Name: **Typesetter**
System: **APPLE II OR APPLE II Plus**
Memory: **32K**
Language: **Applesoft II and Machine**
Hardware: **Disk II**

Description: The Typesetter is a complete HIRES character generating and editing system. It features foreground and background colors, upper/lower case, inverse video, rotated characters, and foreign characters sets (including Greek, Hebrew, and PET graphics). Characters may be positioned anywhere on the screen, eliminating the usual 40X24 grid. The output is through regular print statements. Scale, color, and other functions are implemented using standard Applesoft II commands. Use it to label graphs, create ad displays, or print lower case or foreign languages. A character set editing program is included. Character tables are compatible with Apple's character generator on user contributed Volume 3. The system includes 35 utility programs and character sets plus manual.

Copies: **30**
Price: **\$24.95 on diskette. Please specify disk or ROM Applesoft. N.C. residents add 4 percent sales tax.**
Authors: **Jeff Schmoyer and Joe Budge**
Available: **ANDROMEDA COMPUTER SYSTEMS**
P.O.Box 19144
Greensboro, N.C. 27410
(919) 852-1482

Name: **Morse Code Transceiver Program**
System: **Ohio Scientific C1-P and Superboard 2**
Memory: **Standard 4K**
Language: **Machine Language and Basic**
Hardware: **Decoded Port Required (schematic supplied)**

Description: The program is designed for the HAM that wants a truly useful morse code program. It will copy CW up to 60 WPM. The copy program tracks the incoming code speed and, therefore, the user needs only to set the transmit speed. The program comes up in receive mode and is ready to copy. To go to transmit mode, simply press the spacebar. A cursor will now appear in the upper left hand corner of the screen. This is the position of the character that is presently being sent. As characters are entered from the keyboard they will be displayed across the screen. After each character is sent, the display is updated by a fast machine language routine which moves all the characters over one position. While in transmit mode, the following keys have special meaning: ; (semicolon) returns to receive mode ; (colon) program will ask for a change in code speed. RUBOUT key will backspace cursor in order to easily make corrections.

Price: **\$14.95 prepaid, M/C and VISA accepted**
Includes: **Program on cassette, loading instructions, operating instructions, detailed instructions and schematic for building required port.**
Author: **Steve Olensky, WB4DCL**
Available: **Olensky Bros., Inc.**
Computer Sales Division
3763 Airport Blvd.
Mobile, AL 36608

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ANA1 (ANALYSIS 1) is a set of BASIC Programs which enables the user to perform analyses on the Dow Jones Industrial weekly average data. From 6 months to 5 years of user selected DJI data can be plotted on the entire screen in one of 5 colors using Apples' High Resolution capabilities. The DJI data can be transformed into different colored graphic representations called transforms. They are: user specified moving averages, a least squares linear fit (best straight line), filters for time, magnitude, or percentage changes, and user created relationships between the DJI data, a transform, or a constant using +, -, x, / operators. Colored lines can be drawn between graphic points. Graphic data values or their dates of occurrence can be displayed in text on the screen. Any graph or text can be outputted to a users printer. The Grid Scale is automatically set to the range of the graphs or can be user changed. As many colored graphs as wanted can be plotted on the screen and cleared at any time. The user can code routines to operate on the DJI/transform data or create his own disk file data base. ANA1 commands can be used with his routines or data base. An Update program allows the user to easily update the DJI file with current DJI weekly data.

The ANA1 two letter user commands are: CA = Calculate, no graph. CG = Clear Graphs, leave Grids. CK = Checking out program, known data. CO = Color of next graph (red, green, violet, white, blue). CS = Clear Screen. DL = Draw Line between points. FI = Filter data for time, magnitude or percent change. FU = Data, transform, or constant Function with +, -, x, / operator. GD = Graphic mode, display all Graph Data on screen. GR = Graph data to screen. GS = Set Grid Scale. HE = Help, summary of any commands usage. LD = Load Data from disk file from inputted date to memory. LG = Leave Graphs, automatic Grid rescaling. LO = Look, select a range of the LD data and GR. All commands can now be used on this range. LS = Least squares linear fit of the data. MA = Moving Average of the data. NS = No Scale, next graph on screen does not use Grid Scale. NT = No Trace. PR = User implemented Printer routine. TD = Text mode, display Text Data on screen. TI = Time number to date or vice versa. TR = Trace. TS = Text Stop for number of lines outputted to screen when in TD. U1/U2 = User 1/2 implemented routines. VD = Values of Data outputted in text. VG = Values of Grid; low/high/delta. VT = Values of Transform outputted in text.

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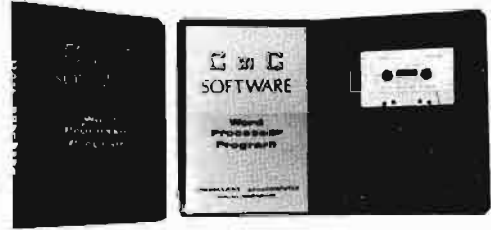
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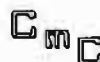
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List your program page by page.

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Rockwell has introduced a bubble memory board for 128 kbytes of storage which plugs directly into the expansion bus for the AIM-6502 processor (same as for KIM-1), expandable to 16 such memory boards (2 Mbytes).
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Adds a program relocater, a program listing utility and a trace function.

Morris, E.D., Jr. "Replace that PIA with a VIA" pg. 17-18.
If your board uses the 6520 PIA, try replacing it with a 6522 VIA to get all the functions of the 6520 plus two timers, a shift register, input data latching and a much more powerful interrupt system.

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No more lost files, missing data, etc. with this improved I/O.

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An easy to build EPROM board requires no special interfacing.

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A program to easily generate and modify Hi-Res characters on the Apple II.

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Seiler, Bill. "PET Renumber 3.0," pg. 12-14.
A useful utility for the PET.

Martinez, Henry. "PET IEEE-488 to SWTPC PR40 Printer Interface," pg. 18.
Hardware for the printer interface.

Butterfield, Jim. "Memory Usage and Garbage Collection," pg. 18.
Tips on Memory Usage.

Riley, Michael. "Panic Button," pg. 21.
Short machine language routine to help regain control of the cursor.

Butterfield, Jim. "Arrow," pg. 24.
A game for the PET.

554. Call -Apple 2, No. 6. (July/August 1979)

Golding, Val J. "A HEX on Thee," pg. 4-6.
A discussion of Binary, Hex and different number systems involved in the Apple II. Includes a HEX-DEC Converter Basic program.

Wagner, Roger. "A Fast GR Screen Clear," pg. 8.
Clear the low resolution graphics page of the Apple very fast.

Aldrich, Darrell. "BADR.CREATE," pg. 8.
A program for the Apple to give the start and length of a BLOADEd file.

Aaronson, Tim and Hertzfeld, Andy. "Using Page 2 Text and Lo-Res," pg. 13.
Routines for special effects on the Apple II.

Anon. "IMA-A new Computing Language," pg. 13.
IMA is a new language by Microversity which allows the use of Integer Basic, Machine Language and Applesoft in the same program.

Garson, David B. "Multiply Demo," pg. 19.
Routine to show the use of the multiply function in the Apple's monitor.

Aldrich, Darrell. "Color Twentyone," pg. 21.
Software approach to creating additional Hi-Res colors.

Golding, Val J. "Hidden Rem Formatter," pg. 21.
Two programs for hidden rams.

Garson, David B. "Soul Searching with the Apple," pg. 22.
A machine language program to go through memory looking for occurrences of HEX or ASCII strings that the operator specifies. For the Apple.

Koftinoff, Jeff. "Bowling," pg. 24-25.
A well arranged and documented listing for a game of bowling.

Aldrich, Darrell. "The Apple Doctor," pg. 26.
How to verify a ROM in your computer. Also a discussion of the new AUTO-START ROM and how to put it on the Applesoft Firmware Card to achieve optional Autostart action. This way you retain the old ROM and the functions that would have been lost such as STEP, TRACE, etc., that are in the old monitor.

Thyng, Mike. "Applemash," pg. 28.
Discussion of a project to get an IMSAI and the Apple II to talk to each other.

Rivers, Jerry. "Amazing Mystery Program," pg. 30.
A short program for the Apple.

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While evaluations are important, the main focus of Creative Computing magazine is computer applications of all kinds. Many of these require that data be retrieved or sorted. Unfortunately, most programming texts focus on the bubble sort (or straight insertion) and, very infrequently, another technique (usually delayed replacement) and let it go at that.

Yet, except for comparison counting, the bubble sort is the least efficient. Tutorials and articles in Creative Computing demonstrate that the Shell-Metzner and Heapsort are from 50 to 13,000 times as fast as the bubble sort! Consider a sort of 100,000 items on a DEC System 10:

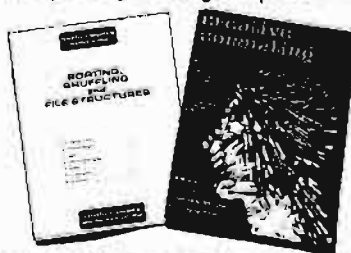
Bubble sort	7.1 days
Delayed replacement	3.8 days
Heapsort	17.3 minutes
Shell-Metzner	15.0 minutes

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Because sorting and shuffling (mixing a list of items) is so vital in most programming, we are making available a 20-page reprint booklet on Sorting, Shuffling and File Structures along with our May 1979 issue which has several articles on writing user-oriented programs and making the most of available memory space. The reprint booklet and issue are free with 12-issue or longer subscriptions.

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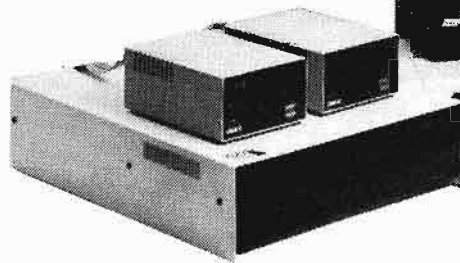
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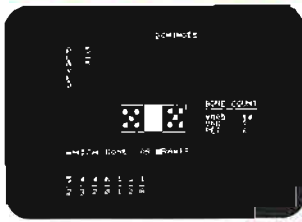


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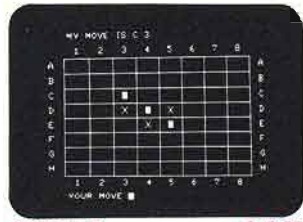
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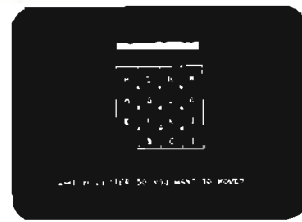
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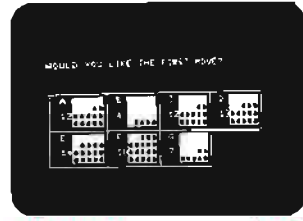
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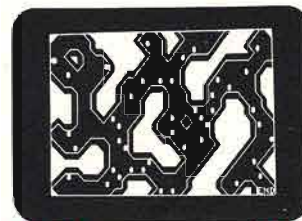
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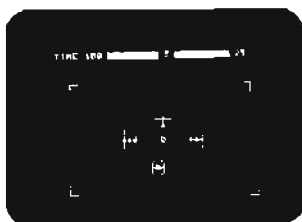
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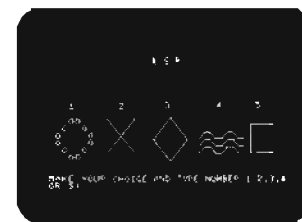
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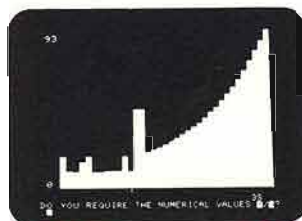
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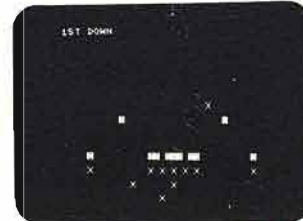
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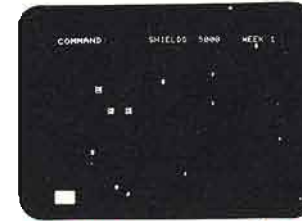
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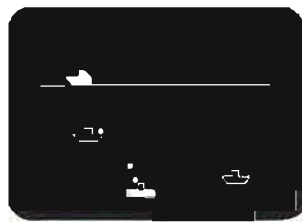
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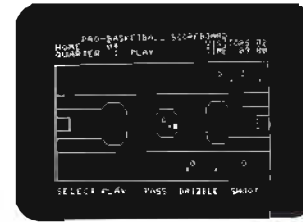
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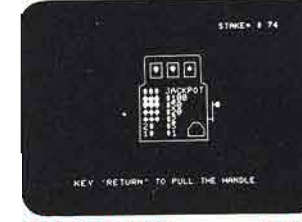
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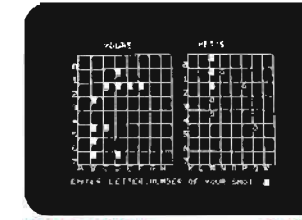


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